OS-9 SYSTEM PROGRAMMER'S MANUAL VERSION 1.1

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TABLE OF CONTENTS

eş	IN	I	R	0	DI	J (T	I	0	N	ا ام	r	0	(3	~	9			V	E	I	•	I	- I	777		-	•	-	_		_	_	_	. =	-	-	_	_	-	1 1
	На	S	v	3	ti	en o	. J 1	Н	2	11. To	d:		ים המ	re) <u>.</u>	8	-	n 1	1 i		-	177	2	n	y.	+J				_			_	_	_	-	•	-	456	-	-	3
																																					. i					
																												-			401								-			4
1	MT	ΙĮ	T	I	P.	RC) G	R	A	Μ	M	I	N (3		_			-	-	-		#		_	_		-	•	-	-			-	-	-	• 1	-	_	-	-	6
		F	, L	0	C	2 5	5 5		C	r	9	a	ti		מו	1		-		-	•	•	-			-	•		•	-	-	•	-	-	-	-	-	-	-	-	-	6
		F	r	0	C	e	S		S	t	a	t	es	5 .		_			•	-		•	***			-	•	_	. •	-			-	-	-		•	_	- -	***	-	7
		I	I	e	C	u t	i	0	n		S	C	h e	ed	lυ	ıl	1	ne	3		-			-4	-	-		_	٠	-			-	-	-	-	•	-	-	-	_	8
						a]									•	-																										9
1	ΜI	٨	10	R	Y	٢	1 A	N	A	G	E	4	EI	V]		_		 -			-	•	-		-	-		-	•	-	-		-	_	-	-	•	_	·	_	483	10
			10	111	n :	19 %	•	Α	1	1	Λ.	~	a 1	١i	ា	'n					-		-			-				-	_			-		-	•	-	-	-	-	10
		T	y	P	1	ca	1	4	M	е.	m	0	ry	7	۲	a	p		•		-		-	111	-		15.	-		-	-		-	-		-			-	-	_	11
		۲	le	m	0	r J	7	M	0	ď	u.	1	e :	5		_			-		-		-	•	-	-		-	•	-	-	•	-	-		-	•	-	-	-		12
		۲	le	m	0 1	ry	•	M	0	ď	u.	1	е	S	t	r	u	C	t	ï	.6		_		7			-	•	-	-		7	-	-	-	•	-	-	-	-	13
	٠	M	10	đ	u.	l e	•	H	e	a	d (е:	r	I	e(Î	1	n:	. ;	, 1	C	Π	S			-	• 1	-	•	-	-		-		_	-	•	-	430	***	_	14
		1	У	P	e	ì.	M	Ю	ď	u	1	е	F	Ξe	a	₫	e	T:	5 .		-			_	-	7	•		•	-	-	•	-		-		•	-	-	-	-	16
		I	X	е	CI	u 1	a	D	1	e	- 1	M	er	77 C	r	У	<u>.</u>	M	0.0	lu	1	e		ŀ	0 I	[1]	a	t	1)1	a	8	ra	III		-	•	-		-		17
																																							-			18
•	วร		.9		S	YS	T	E	M		C	01	ΜI	90) N	E	N	TS	3		_		-			-		_		-	_				_	-	•	-	-	_	-	19
		T	'n	e	1	Κε	r	'n	e	1			_	_	-	_				-	_					-		_	٠.	_	-	•	-	-	-	-		-	-		-	19
		-	1	•	- 1	٠,	'n	P	÷.	-			s t	- 4	_	n		M,	` A	**	1	Δ.			-	_			٠.	-	-	٠.		-		-		-		-	-	20
		Ą	174	0		₹ ₹	7 6	•	0	77		Q,	0	n t	٠.	+	T	21	2	~	0	A	11	7	۵.	-		-		-	-		-	_	-	٠.		400	4000	-	-00-	21
		n	ıt.	h	01	•	T	m	ħ	0	۳,	t	a T	٦ŧ		М	0	٦,	1]	6	2		-			-			-	-	-		-	-	-	-		-	-	-	-	22
		R	u	n	n	in	ıg	,	Ō	S	-{	9	1	1 5	;	a		R	1(1	B	a	S	e	1	S	y	S	te	211	1, -		-		-	-		_	-	_	-	22
•	ľE	Ξ	,	0	S٠	-ç)	Ι	/	C	-	S:	YS	I	E	M		-	•	-	-	e e	-	•	-	-	•	-	-	-	-	•	-	-	-		•	-	423	-	-	24
		I	n	t	r	o d	u	C	t	i	C I	n	1	C	, .	t	h	e	I	./	0		S;	y	s t	e	M		•	***	-	•	-	-	-		• • •	C	-	7	_	24
		I	n	p	u '	t	1		0	u	t	pı	u t		M	a	n	a e	5 6	r		٠.	-	•	·			-		- '	-	٠	-	•	_	-	•	-	-	-	-	25
		F	1	1	e	٢	a	n	a	ε	e	r:	S	-	-	7	•	-	-	•	-		_	٠.,	-	-	•	-	•	-	-	•	-		-	-	• , •			-	_	25
		D	e	V.	10	CE	•	D	r	i	V (2	r	٢	10	đ	u	1 6	2 5		****			•	-	-		-	•	-	***	•		-				- ,	-	***	_	26
		D	e	٧	i	2 6	•	D	e	S	C:	r:	iĮ) t	0	r		M) d	u	1	e	S	•	-	-		-		-	-	•		-	-	-	• •	-	_	-	_	27
		D	e	٧	1	3 6	•	D	е	S	C	r !	LI	t	0	r		Mc	d	u	1	e		F	or	m	a	t	1	ì	a	E1	ra	m	_		•		-	-	-	28
		P	a	t.	a ·	Ι)e	S	C	r	1]	b.	tc	r	' S			-	-	•	_		_	•	-	-			-	-			-	-	-	***		-	-	-	-	32
		R	a	n	d (חכ	ì	В	1	0	c)	K	I	i	1	e	1	Ma	1 T	a	g	e	r		(F	В	F	M	AN	()			-	-		-		air.	-	-	-	33
				L	೧ಚ	7 1	C	a	1		a 1	10	7	F	יתי	V	ς.	10	` A	1		ת	1 1	S	•	ഹ	T	27.7	1	٦i	7.	a 1		OI	7	-		-	-	-	-	34
					- 3	[d	e	n	t	i	f:	Ļ	c a	t	i	0	n.	S	έ	c	t	0	r	•	-	_		-	•	-	-		-	_	-			-	-		-	34
					1	ji	S	X		A	1	1	C C	a	t	i	0	n	1	1a	p		S	e	c t	C	r			_	-		-	-	_			-	_	-	-	34
]	: 1	1	e		D	e:	5 (CI	i	p	t	0:	r	S	ε	C	t	0 1	r	S	-		÷.		_	-	•	-	-	-			-	-	-	- ,	35
]	ji	r	e	С	t	¢:	r	iε	25	, -		٠,	_	-	- '	-				_			-	-	- '			-	-	-			-	-		-	36
				R.	B	F۲	A	N		P	a ·	t l	h	Ι)e	s	C	r	F	t	0	r		D	ef	ì	n	i	ti	Lo	n	S		-	-	-		(11)	-	•	-	36
		S	e	Ğ.	u e	en	t	1	a	1		Cl	n a	ı	٠a	С	tı	e 1		F	i	1	e	1	Ma	n	a	ge	2 1		-	/. ·	_	_	_	-		44.3	-	· '.	_	38
				S	C:	EN	!A	N		P	a ·	tl	1	Ι)e	S	C.	r	7	t	٥	r		D	ef	i	n	i	tj	C	n	S		_	-	-		-	-	_	-	38
				S	C)	E۲	A	N		L	i	מ	e	E	d	i	t	iı	ع د		F	e	a	t	uI	e	5			-			-	-	_	-		-	_	_	_	40

TABLE OF CONTENTS - CONTINUED

IN	Ü	¥	R.) T	P	ىلە		ÞΈ	0	C.	F. 9	5.5	T	N (·								-	***		-			- '	-	eco .	-	-	•		•	45 0	4	12
* 11	7		₩.			-		~ ~	. +	;	_ `	מ	-	1	13.	•	_	c	77	e 1		m		_						4000		-			-		-		14
	1	T.	٧.		u		O:	II C	! L	_	C.	r	0	1	4,±	TI (5.	د.	y	۱ د ۲) C	111		•		n .		77 6		e .		_				_	-		16
	*	r	1	C 1	Į.	g		LI	ιτ	е	rı	ru	F	ſ,	رد–	1	Τ1	/ E	11	1	<i>)</i> E	V	Τ,	. E		נע	7	۷ ۲	= 1	2	7							•	ro
						-				_			_												2.														47
WI	1	t	1)	ع ۵	-	A	5	SE	m	b	13	7	L	a:	ng	u	2 6	36		Ρı	0	g	r ć	lm,	S			•	a	-	-	-	-	•		-	-		
		_		_	_	77	-	_ z	_	-		•		•	~ ~	-	_	`~	_	n	•	f .	~ ^			_	_		-	-			-	•		-	-		47
Jan 4	C	+	2 1	'nÀ	ء ا	*	7	· .T	'n	n	11 1	t /	'n	77	t۳	77	Ť.	P	2	7. "	: 5			-		-	4			-	4535	CHES		. •	-		acm.		47
																																						. 4	48
	N	13	<u>-</u>	hi				<u>ر</u> د		۲	ŀ	R	0	<u> </u>	וווי	7	et	ne	ווי	t (Ξ,		_		-			_	_	-	-		٠.	-	-	-	. 4	48
	+		+ /				<u> </u>	+	, u	=	45 C 1	e ic		4	~ _			-			,		-	-		454	-				47500	-			_	*****	-		48
	7	ш	ا ن ڌ.	= 1	. 1	u	P	ν -	- 15	a):		,								- '								_	_	_	_			_	- :	-		49
	ľ	I	a	11]	ני	. 6		P 1	C	6	Γ	317	1		_			-		_	-		_	-		_	_			_								•	I.J
2 3	١,						1									_							_																50
05	-	-9	8 1	Se	2 1	V	1	CE	2	R	e	qu	16	S	t	D	e:	5 0	r	1]	pt	1	C	15		-	-	•		C30	· em	-	400		-	-	-		שכ
			٠.																																				
	1	Is	۾			10	đ	۾	Ţ	'n	n (ct	1	0	n	R	e	ານ	ie	s 1	t	D	e:	sc	r	i:	t	1	on	S	-	_	-		_	-	-		52
							T		9	6	+	• •	٦i	Ť.	ς.	1	n	2	n		a 1	1	0	CA	ı t.	10	nc		- b	11		na	0		- ,	-	-		52
				•			N		ິດ	h	3		` -	+	~	_	 1	n		,	ת ה	_	0	ra	m	٠,,,	_			_	-		_		-	-	-		53
									۲	44	<u>a</u> .	11	1 -	•	•		_ ′	, C		-	5 T	٧	5			_ ;			_	_			_		_		-		55
							M		. (0	m	pe	1 [Ε.		W	0	Ţ	īŒ	1010	: 3		_	_		_			_ :		_	_			-	-	-		56
									(0	m.	ρι	1 t	е	٠	H	U	Ì	ā	Ţ	ue	•	_						_		_		-		_				57
							T		I	e	a		LO	C	8	; e		11	1	a.	C	1	t	Π	ıa	D.	-	•	_	_			_		-		-		
			F	\$.	\mathbf{E}		T		ा		-	- 1	•	•		١.	•	- 7		4 1	~ ~		~ '	n r	•	0			_		- 4042	-	- 400	•	_	-			58
			F	\$]	F(E	K		. C	r	P	a 1	t e	• `	а	n	e١	W	D	r	00	e	S	5		-	-	• •	-	-	04000	-	-		-	6000 -	-		59
			P	ġ.	T () F	T		(6	+		•		2	•	i .	CY	12	7	·i	77	1.	er		PT	ጎ 🕇		†. T	יהי	3.79	200	-	•	_	-	-	1	61
							-		~	_	+			^	^6		C	٠.	/	33 1				TΤ	١.	_	-			-		9	-	. :	434	430	-	-30	62
							K		T	ĭ	, m		+	^	7		m	, 7	·	. ,	n c	A	77	۱ د	•		_			_	•	-	-		-	CC300	435	- (63
				7					7		. i.e	n.	~		بر در تم	. 7) e	3	٠.	e		m		-	e i	1	_		_			_		-		•		64
				•			D		7	O	di	ŭ.	I.	IU	2	4.4	E	(=	3 · /				111		۱ . د م	1 1		_		•						_			65
				•			1		Ť	e	5	12	3 E		ac	l L	a	F	: 6	Hi (O T	ý	. 1	d I	E	ď	_	-	Τ.	_		_			-				66
							R		Ţ) Y	1	71 1		0	T I	ຳດ	*	T	೧၉	ς .	58	ے ا	Θ,	_	-	-	-	•	-	47230	-	-	-	•	_				
			F	\$]	P !	V.	M		~	١.	_		_	-	_		•		•	-	-		_	-	_	_	_		_	-	-	-	-	•	_	-	400	. 1	67
			F	\$	SI	B1	T				. ~	m,	n h		-	•	2	1	_^	~	- 7		· •	77	- n	٦.	т.	m	аτ	١.	-	-	•		-	-	-		68
							ID		ç	ė	n	ď	ā	ı	Si	le	n	a.	į	t	0	a	n	0 1	th	e:	r	p	rc	C	2 5	S .	-	•	_	-	-	اعدد	69
				•			P		1	277	+	٠,	٠.	1	1 1	'n	0		- 1 7 *	n.	C 6	· S	ς	ŧ	iο	•	51	e	et	·	_	-	-	-	-		-		70
							Ī			. u	. •		, U		~ 2		5		7	i	0 t	. i	+	17		_	-			_			_		-	-	-		71
				•					-	-		ا د	7,	7	٠.		s.				4 .	•		J To c		•••	~ ~			_					-		-		72
							C		4	. II	5	t	3 1	. 1	•	1	+	u.	10	·	7.) II		T C	= 4	u	C 3	, ,	Ŀ		_			_	_	-			74
							'I																																75
							[[5	e e	t	•	5 y	S	te	3 M) - 1	dē	Ţ	е	ĉ	n	d	1	1	m	9					-			_	_	_		
			F	\$	\mathbf{T}	I	1E	1	- (¦ε	it.		5 %	15	ts	- IT	1	ďá	3 t	e	ć	n	a	. 1	ti	m	е		-	W00		-	•	•	-	-	-		76
			F	\$	U	N]	K		I	Jn	1	i	nk		a	T	0	đ١	11	e		-		-	-		-	•	-	-	•			-	-	-	400		77
							1		¥	la	i	t	f	٥,	r	C	h	1:	ld		to)	d	ie	2	-		•	-	_	-	***		D	-	@>	*		78
- 1			-	Τ.						_		•																											
		2 41	, -	+	_	**	N	١'n.	7 .	_	T	**.	n /	٠+	4 /	וז' ר		p,	٥n	11	6	: †		De	- <	~	ri	Ď	t. 1	0	n s	-		-	-	-	-		79
		J y							u (= N 7	1	u		, u	· C	בי		V. 4	- 4		٠.	/ E	,) =	+}	 1	ת	 o c		7	'n	to	۳.	٠	_	_	-	-		79
							ŧ		1	11	1	0	C	ن <u>۱</u>	Ε.		4	-	~ E	: 3	3 /		a	67	<u>,</u> V	171	C :	, 0		. p	· -	•					-		8ø
				•			? C			510	lt	e.	r	A	C.	į, l	¥	E	1		0 (36	: 5	>	پ	u	۳ <i>(</i>								-				81
							1) e	5	C I	71	PI	0	Γ.		•	•	_	-			
							DI								I,								-	. •	_	-	-	•	•	-		_	•	• ""	-	***	****		82
			F	\$	I	0(Ĵζ	1							1								_	-	-	•••		-	-	-	***	-	• • •	-	-	_	-		83
)			Ad	d	1	De	v	i	CE		t	0	t	he	2	I	RO	Ç	D	0]	11	iı	ig.	t	a t	16	2	-		400		84
							٠ ۲		,	5 1		r	t.	N	le:	r t		p.		ò	e	5 9	;		_	_	-	-		-	<u>, </u>		٠, •	-	_	_	-		85
							4) e	+	,,	,	n ''	D	70.0)	۰ م	- ×	. /	P:	- -	h	1	De	, C	C 1	ri	ימי.	to	r	-			-	-	_		86
																										د . پ	Ĭ.					_		-			_		87
								1							M												-								_				88
		41						1							R							Ti C) ľ	y		-	-	•			-	-	•	_	-	_			89
			- 1		W	M.	17	١.	,	1 -	٠٦.	4	A .	~ +	0		7	~	•• 1			-	-			-		MED .	-	-	-			-	-	-			oz

TABLE OF CONTENTS - CONTINUED

			ACCCDDDGMORRSSW	TOLRITUSDPECESR	CIOEECPTIEALETI	HRSATH TRNDNKTT		ACCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC	thireeueapeeer	tao elt ptkeaapti	ace of the contract of the con	che e e e e e e e e e e e e e e e e e e			or properties of the contract	nra pad ow/	ektaiear Oft	Wihtlyn d r lyt	n hei dipoloio	d st cleramisc	e ot-e/vet niea	vi. o o o ichaec	ir a a frict the state of the s	e e e e e e e e e e e e e e e e e e e	cin mtsy loft	ttle- ht aemiu/	ooew t a / 1sd	ry heti-fidae	th control of the con	e dl s	_ e e - y / c 1 i - e	s) v: deen	/s ice ice /de ice /de	t-ev-mi-er-	e i								0012356782345670111	
API		N])]	X		A	•		W	r	11	c.	٠.		+ 4		•	٠.	. +	_	70	3	CP (_	т	۵۱	s۴	ŧ	יח ל	1	. i	Ω	ns	:		_	-	•	-	-	•	-	 A1 A1 A4	
API	PE	N]	DI	X	S .	В	•		W	r	i	t: S D	i i	n a v	t:	ic	3 C C C	S	i t	-t	y r v	p, a	e g r	e	De I Si	e v O∈	ef	c i o	e n: u	I i i	ri	i o e	ve ns	3 I	•s		-	•	-	est est			B1 B1 B3	
API	PE	N]	D I	[]	5	C	:		'n	r	1	t	i :	n	ಕ	Ų	A		3 y	5	t	e	m		Bo	0	t	s	t	rá	a p)	Mo	o d	lu	1	e		-472	-		~	C1	
AP											4	C	O	n	f:	1,	gı	1.	rć	1	: 1	. 0	n		M	0.0	10	L	е		-	•	_	•	-	***	-	-		-		•	D1 D1 D2 D3	
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INTRODUCTION TO OS-9

OS-9 Level One is a versatile multiprogramming / multitasking operating system for computers utilizing the Motorola 6809 microprocessor. It is well-suited for a wide range of applications on 6809 computers of almost any size or complexity. Its main features are:

- * Comprehensive management of all system resources: memory, input/output and CPU time.
- * A powerful user interface that is easy to learn and use.
- * True multiprogramming operation.
- * Efficient operation in typical microcomputer configurations.
- * Expandable, device-independent unified I/O system.
- * Full support for modular ROMed software.
- * Upward and downward compatability.

This manual is intended to provide the information necessary to install, maintain, expand, or write assembly-language software for OS-9 systems. It assumes that the reader is familiar with the 6809 architecture, instruction set, and assembly language.

HISTORY AND DESIGN PHILOSOPHY

OS-9 is one of the results of the BASIC09 Advanced 6809 Programming Language development effort undertaken by Microware and Motorola from 1978 to 1980. During the course of the project it became evident that a fairly sophisticated operating system would be required to support BASIC09 and similar high-performance 6809 software.

OS-9's design was loosely modeled after Bell Telephone Laboratories' "UNIX" operating system, which is becoming widely recognized as a standard for mini and micro multiprogramming operating systems because of its versatility and relatively simple, yet elegant structure. Even though a "clone" of UNIX for the 6809 is relatively easy to implement, there are a number of problems with this approach. UNIX was designed for fairly large-scale minicomputers (such as large PDP-11s) that have high CPU throughput, large fast disk storage devices and a static I/O environment. Also, UNIX is not particulary time or disk-storage efficient, especially when used with low-cost disk drives.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Introduction to OS-9

For these reasons, OS-9 was designed to retain the overall concept and user interface of UNIX. but its implementation is considerably different. OS-9's design is tailcred to typical microcomputer performance ranges and operational environments. As an example, OS-9, unlike UNIX, does not dynamically swap running programs on and off disk. This is because floppy disks and many lower-cost Winchester-type hard disks are simply too slow to do this efficiently. Instead, OS-9 always keeps running programs in memory and emphasizes more efficient use of available ROM or RAM.

OS-9 also introduces some important new features that are intended to make the most of the capabilities of third-generation microprocessors, such as support of reentrant, position-independent software that can be shared by several users simultaneously to reduce overall memory requirements.

Perhaps the most innovative part of OS-9 is its "memory module" management system, which provides extensive support for modular software, particularly ROMed software. This will play an increasingly important role in the future as a method of reducing software costs. The "memory module" and LINK capabilities of OS-9 permit modules to be automatically identified, linked together, shared, updated or repaired. Individual modules in ROM which are defective may be repaired (without reprogramming the ROM) by placing a "fixed" module with the same name, but a higher revision number into memory. Memory modules have many other advantages, for example, OS-9 can allow several programming languages to share a common math subroutine module (such as Motorola's new MC6839 floating point subroutine ROM). This same module could automatically be replaced with a module containing drivers for a hardware arithmetic processor without any Change to the programs which call the module.

Users experienced with UNIX should have little difficulty adapting to OS-9. Here are some of the main differences between the two systems:

- 1. OS-9 is written in 6809 assembly language, not C. This improves program size and speed characteristics.
- 2. OS-9 was designed for a mixed RAM/ROM microcomputer memory environment and more effectively supports reentrant, position-independent code.
- 3. OS-9 introduces the "memory module" concept for organizing object code with built-in dynamic inter-module linkage.
- 4. OS-9 supports multiple file managers: modules that interface a class of devices to the file system.
- 5. "Fork" and "Execute" calls are faster and more memory efficient than the UNIX equivalents.

OS-9 LEVEL CNE SYSTEM PROGRAMMER'S MANUAL Introduction to OS-9

SYSTEM HARDWARE RECUIREMENTS

The OS-S operating system is made up of "building blocks" called modules which are automatically located and linked together when the system starts up. This makes it extremely easy to reconfigure the system. For example, reconfiguring the system to handle additional devices is simply a matter of placing the corresponding modules into memory. Because OS-9 is so flexible, the "minimum" hardware requirements are difficult to define. A bare-bones LEVEL I system requires 4K of ROM and 2K of RAM, which may be expanded to 56K RAM. A large LEVEL II system might have several hard disks, multiple terminals, and a megabyte of RAM using memory management hardware.

Below are the requirements for "typical" OS-9 computer systems. Actual hardware requirements may vary depending upon the particular application.

- * 6809 MPU
- * 4K Bytes RAM Memory for Single Board Computer Systems
 24K Bytes RAM Memory for Assembly Language Software
 40K Bytes RAM Memory for High Level Languages such as BASIC09
 (RAM Must Be Contiguous From Zero Up)
- * 4K Bytes of ROM: 2K must be addressed at \$F800 \$FFFF, the other 2K is position-independent and self-locating.

Some disk systems may require three 2K ROMs.

Single board computers and other ROM based systems may require an additional 6K of ROM for modules which are normally loaded from the bootstrap file (6K includes SHELL and DEBUG modules).

- * Disk or cassette tape I/O device.
- * Console terminal and interface using serial, parallel, or memory mapped video.
- * Optional printer using serial or parallel interface.
- * Optional real-time clock hardware.

I/O device controller addresses can be located anywhere in the memory space, however it is good practice to place them as high as possible to maximize RAM expansion capability. Standard Microware-supplied OS-9 packages for computers made by popular manufacturers usually conform to the system's customary memory map.

THE KERNEL AND ITS BASIC FUNCTIONS

The heart of OS-9 is called the "kernel" which serves as the system's administrator, supervisor, and resource manager. It is about 3K bytes long and normally resides in ROM with 2K at the highest memory addresses (\$F800 - \$FFFF). Its main fuctions are:

- 1. System initialization after restart.
- 2. Service request processing.
- 3. Memory management.
- 4. MPU management (multiprogramming).
- 5. Interrupt processing.

Notice that input/output functions were not included in the list above; this is because the kernel does not directly process them. Instead, there is a separate I/O system constructed from a number of standard (or user-supplied) program modules selected to match the computer's specific hardware configuration. This is why OS-9 can be easily "tailored" to almost any 6809 computer's hardware configuration. The kernel passes I/O service requests directly to another subsystem called the "Input/Output Manager", or "IOMAN". Its function is to decode the I/O service request to select a specific "file manager" and a specific "I/O driver", which do the actual processing. The file managers and I/O drivers are also modules selected for the specific system configuration.

After a hardware reset, the kernel will initialize the system which involves such things as locating ROMs in memory, determining the amount of RAM available, loading any required modules not already in ROM from the bootstrap device, and other related tasks.

Service requests (system calls) are used to communicate between OS-9 and assembly-language-level programs for such things as allocating memory, creating new processes, etc. In addition to these callable functions, there are other "real-time" functions such as time-slicing, timekeeping, and interrupt service, which are automatic and occur routinely during normal system operation.

Memory management is a very important operating system responsibility. One way in which OS-9 is different than other operating systems is that it manages both the physical assignment AND the logical contents of memory, by using entities called memory modules. All programs are loaded in memory module format, allowing OS-9 to maintain a director, which contains the name, address, and other related information about each module in

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL The Heart of OS-9

memory. These structures are the foundation of OS-9's modular software environment. Some of its advantages are: automatic runtime "linking" of programs to libraries of utility modules; automatic "sharing" of reentrant programs; replacement of small sections of large programs for update or correction (even when in RCM); etc.

OS-9 is a multiprogramming operating system, which means that several independent programs called "processes" can be executed simultaneously. Each process can have access to any system resource by issuing appropriate service requests to OS-9. Multiprogramming uses a hardware real-time clock that generates interrupts at a regular rate of about 10 times per second. MPU time is therefore divided into periods typically 100 milliseconds in duration. This basic time unit is called a "tick". Processes that are "active" (meaning not waiting for some event) are run for a specific system-assigned period called a "time slice". The duration of the time slice depends on a process's priority value relative to the priority of all other active processes. Many OS-9 service requests are available to create, terminate, and control processes.

Interrupt processing is another important function of the kernel. All hardware interrupts are vectored to specific processing routines. IRQ interrupts are handled by a prioritized polling system which automatically identifies the source of the interrupt and dispatches to the associated user or system defined service routine. The real-time clock will generate IRQ interrupts. SWI, SWI2, and SWI3 interrupts are vectored to user-definable addresses which are "local" to each procedure, except that SWI2 is normally used for OS-9 service requests calls. The NMI and FIRQ interrupts are not normally used and are vectored through a RAM address to an RTI instruction.

At any given moment, OS-9 is in one of two states: "user state" when a user process is in execution, and "system state" during execution of OS-9 routines which occurs after any system service request cr a hardware interrupt.

MULTIPROGRAMMING

This section of the manual deals with the following aspects of multiprogramming under the OS-9 operating system: process creation, process states, execution scheduling, and signals as a means of inter-process communication.

PROCESS CREATION

New processes are created when an existing process executes a "fork" service request. Its main argument is the name of the program module (called the "primary module") that the new process is to initially execute. OS-9 first attempts to find the module in the "module directory", which includes the names of all program modules already present in memory. If the module cannot be found there, OS-9 usually attempts to load into memory a mass-storage file using the requested module name as a file name.

Once the module has been located, a data structure called a "process descriptor" is assigned to the new process. The process descriptor is a 64-byte package that contains information about the process, its state, memory allocations, priority, queue pointers, etc. The process descriptor is automatically initialized and maintained by OS-9. The process itself has no need, and is not permitted to access the descriptor.

The next step in the creation of a new process is allocation of data storage (RAM) memory for the process. The primary module's header contains a storage size value that is used unless the "fork" system call requested an optionally larger size. CS-9 then attempts to allocate a CONTIGUOUS memory area of this size from the free memory space.

If any of the previous steps cannot be performed, creation of the new process is aborted, and the process that originated the "fork" is informed of the error. Otherwise, the new process is added to the active process queue for execution scheduling.

The new process is also assigned a unique number called a "process ID" which is used as its identifier. Other processes can communciate with it by referring to its ID in various system calls. The process also has associated with it a "user ID" which is used to identify all processes and files belonging to a particular user. The user ID is inherited from the parent process.

Processes terminate when they execute an "EXIT" system service request, or when they receive fatal signals. The process termination closes any open paths, deallocates its memory, and unlinks its primary module.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Multiprogramming

PROCESS STATES

Each process can be in one of three states:

ACTIVE - The process is active and ready for execution.

WAITING - The process is suspended until some event occurs.

SLEEPING - The process is suspended for a specific period of time.

There is a queue for each process state. The queue is a linked list of the "process descriptors" of processes in the corresponding state. State changes are performed by moving a process descriptor to another queue.

The Active State:

This state includes all "runnable" processes, which are given time slices for execution according to their relative priority with respect to all other active processes. The scheduler uses a pseudo-round-robin scheme that gives all active processes some CPU time, even if they have a very low relative priority.

The Wait State:

This state is entered when a process executes a WAIT system service request. The process remains suspended until the death of any of its descendant processes, or, until it receives a signal.

The Sleeping State

This state is entered when a process executes a SLIEP service request, which specifies a time interval for which the process is to remain suspended. The process remains asleep until the specified time has elapsed, or until a signal is received.

OS-9 LEVEL CNE SYSTEM PROGRAMMER'S MANUAL Multiprogramming

EXECUTION SCHEDULING

The kernel contains a scheduler that is responsible for allocation of CPU time to active processes. OS-9 uses a scheduling algorithm that ensures all processes get some execution time.

All active processes are members of the "active process queue", which is kept sorted by process "age". Age is a count of how many process switches have occurred since the process' last time slice. When a process is moved to the active process queue from another queue, its "age" is initialized by setting it to the process' assigned pricrity, i.e., processes having relatively higher priority are placed in the queue with an artificially higher age. Also, whenever a new process is activated, the ages of all other processes are incremented.

Upon conclusion of the currently executing process' time slice, the scheduler selects the process having the highest age to be executed next. Because the queue is kept sorted by age, this process will be at the head of the queue. At this time the ages of all other active processes are incremented (ages are never incremented beyond 255).

An exception is newly-active processes that were previously deactivated while they were in the system state. These processes are noted and given higher priority than others because they are usually executing critical routines that affect shared system resources and therefore could be blocking other unrelated processes.

When there are no active processes, the kernel will set itself up to handle the next interrupt and then execute a CWAI instruction, which decreases interrupt latency time.

SIGNALS

"Signals" are an asynchronous control mechanism used for inter-process communication and control. A signal behaves like a software interrupt in that it can cause a process to suspend a program, execute a specific routine, and afterward return to the interrupted program. Signals can be sent from one process to another process (by means of the SEND service request), or they can be sent from OS-9 system routines to a process.

Status information can be conveyed by the signal in the form of a one-byte numeric value. Some of the signal codes (values) have predefined meanings, but all the rest are user-defined. The defined signal codes are:

- Ø = KILL (non-interceptable process abort)
- 1 = WAKEUP wake up sleeping process
- 2 = KEYBOARD ABORT
- 3 = KEYBOARD INTERRUPT
- 4 255 USER DEFINED

When a signal is sent to a process, the signal is noted and saved in the process descriptor. If the process is in the sleeping or waiting state, it is changed to the active state. It then becomes eligible for execution according to the usual MPU scheduler criteria. When it gets its next time slice, the signal is processed.

What happens next depends on whether or not the process had previously set up a "signal trap" (signal service routine) by executing an INTERCEPT service request. If it had not, the process is immediately aborted. It is also aborted if the signal code is zero. The abort will be deferred if the process is in system mode: the process dies upon its return to user state.

If a signal intercept trap has been set up, the process resumes execution at the address given in the INTERCEPT service request. The signal code is passed to this routine, which should terminate with an RTI instruction to resume normal execution of the process.

NOTE: "Wakeup" signals activate a sleeping process: they DO NOT vector through the intercept routine.

If a process has a signal pending (usually because it has not been assigned a time slice since the signal was received), and some other process attempts to send it another signal, the new signal is aborted and the "send" service request will return an error status. The sender should then execute a "sleep" service request for a few ticks before attempting to resend the signal, so the destination process has an opportunity to process the previously pending signal.

MEMORY MANAGEMENT

MEMORY ALLOCATION

All usable RAM memory must be contiguous from address 2 upward. During the OS-9 start-up sequence the upper bound of RAM is detemined by an automatic search, or from the configuration module. Some RAM is reserved by OS-9 for its own data structures at the top and bottom of memory. The exact amount depends on the sizes of system tables that are specified in the configuration module.

All other RAM memory is pooled into a "free memory" space. Memory space is dynamically taken from and returned to this pool as it is allocated or deallocated for various purposes. The basic unit of memory allocation is the 256-byte page. Memory is always allocated in whole numbers of pages.

The data structure used to keep track of memory allocation is a 32-byte bit-map located at addresses \$0100 - \$011F. Each bit in this table is associated with a specific page of memory. Bits are cleared to indicate that the page is free and available for assignment, or set to indicate that the page is in use or that no RAM memory is present at that address.

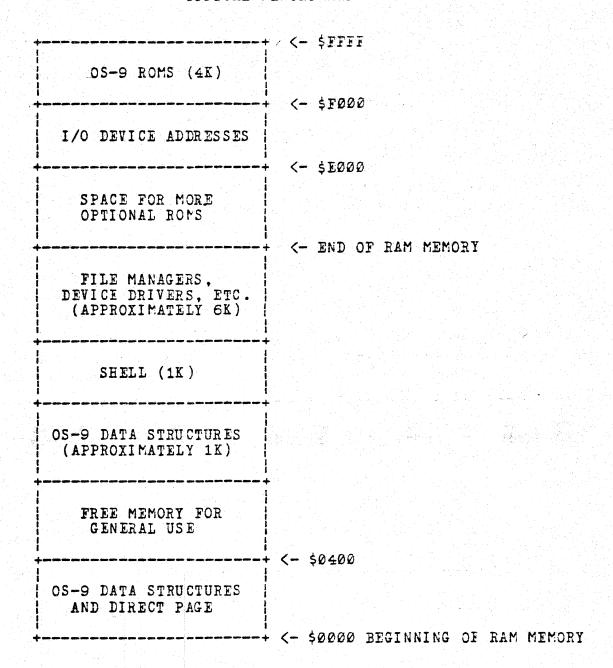
Automatic memory allocation occurs when:

- 1. Program modules are loaded into RAM.
- 2. Processes are created.
- 3. Processes request additional RAM.
- 4. OS-9 needs I/O buffers, larger tables, etc.

All of the above usually have inverse functions that cause previously allocated memory to be deallocated and returned to the free memory pool.

In general, memory is allocated for program modules and buffers from high addresses downward, and for process data areas from lower addresses upward.

TYPICAL MEMORY MAP



The map above is for a "typical" system. Actual memory sizes and addresses may vary depending on the exact system configuration.

MEMORY MODULES

All programs used on OS-9 systems must use the memory module format and conventions. Many of OS-9's extraordinary capabilities would not be possible without them.

The memory module concept allows OS-9 to manage the logical contents as well as the physical contents of memory. The basic idea is that all programs are discrete, named units. The operating system keeps track of modules which are in memory at all times by use of a "module directory". It contains the addresses and a count of how many processes are using each module. When modules are loaded into memory, they are added to the directory. When they are no longer needed, their memory is deallocated and their name removed from the directory (except ROMs, which are discussed later). In many respects, modules and memory in general, are managed just like a disk. In fact, the disk and memory management sections of OS-9 share many subroutines.

Each module has three parts; a module header, module body and a cyclic-redundancy-check (CRC) value. The header contains information that describes the module and its use. This information includes: the modules size, its type (machine language, BASIC09 compiled code, etc); attributes (executable, reentrant, etc), data storage memory requirements, execution starting address, etc. The CRC value is used to verify the integrity of a module.

There are several different kinds of modules, each type having a different usage and function. Modules do not have to be complete programs, or even 6809 machine language. They may contain BASIC09 "I-code", constants, single subroutines, subroutine packages, etc. The main requirements are that modules do not modify themselves and that they be position-independent so OS-9 can load or relocate them wherever memory space is available. In this respect, the module format is the OS-9 equivalent of "load records" used in older-style operating systems.

MEMORY MODULE STRUCTURE

At the beginning (lowest address) of the module is the module header, which can have several forms depending on the module's usage. The header is described more thoroughly later in this section. Following the header is the program/constant section which is usually pure code. The module name string is included somewhere in this area. The last three bytes of the module are a three-byte Cyclic Redundancy Check (CRC) value used to verify the integrity of the module.

MODULE HEADER

PROGRAM
OR CONSTANTS

CRC

The 24-bit CRC is performed over the entire module from the first byte of the module header to the byte just before the CRC itself. The CRC polynomial used is \$800FE3.

Because most OS-9 family software (such as the assembler) automatically generate the module header and CRC values, the programmer usually does not have to be concerned with writing routines to generate them.

MODULE HEADER DEFINITIONS

The format of module headers may seem complicated at first. but in practice they are simple to create and use because OS-9 family software such as BASIC29, the assembler, and many utility programs automatically generate modules and headers. It is a good idea to have a general understanding of what they look like and how they work. The first nine bytes of all module headers are identical:

DESCRIPTION MODULE OFFSET

- \$0.\$1 = Sync Bytes (\$87.\$CD). These two constant bytes are used to locate modules.
- \$2.\$3 = Module Size. The overall size of the module in bytes (includes CRC).
- \$4.\$5 = Offset to Module Name. The address of the module name string relative to the start (first sync byte) of the module. The name string can be located anywhere in the module and consists of a string of ASCII characters having the sign bit set on the last character.
 - \$6 = Module Type/Langauge Type. See text.
 - \$7 = Attributes/Revision Level. See text.
 - 58 = Header Check. The one's compliment of the vertical parity (exclusive OR) of the previous eight bytes.

Type/Language Byte

The module type is coded into the four most significant bits of byte 6 of the module header. Eight types are pre-defined by convention, some of which are for OS-9's internal use only. The type codes are:

- \$1 Program module
- \$2 Subroutine module
- \$3 Multi-module
- \$4 Data module \$5-\$B User-definable
 - \$C OS-9 System module
 - \$D OS-9 File Manager module
 - \$E OS-9 Device Priver module
 - \$F OS-9 Device Descriptor module

NOTE: Ø is not a legal type.

The four least significant bits of tyte 6 describe the language type as listed below:

- Ø DATA
- 1 6809 object code
- 2 BASICØ9 I-code
- 3 PASCAL P-code
- 4 COBOL I-code
- 5-15 Reserved for future use

The purpose of the language type is so high-level language run-time systems can verify that a module is of the correct type before execution is attempted. BASICØ9, for example may run either I-code or 6809 machine language procedures arbitrarily by checking the language type code.

Attribute/Revision Byte

The upper four bits of this byte are reserved for module attributes. Currently, only bit 7 is defined, and when set indicates the module is reentrant and therefore "sharable".

The lower four bits are a revision level from zero (lowest) to fifteen. If more than one module has the same name, type, language, etc., OS-9 only keeps in the module directory the module having the highest revision level. This is how ROMed modules can be replaced or patched: you load a new, equivalent module having a higher revision level. Because all modules locate each other by using the LINK system call which searches the module directory by name, it always returns the latest revision of the module, wherever it may be.

NOTE: A previously linked module can not be replaced until all processes which linked to it have unlinked it (after its link count goes to zero).

TYPED MODULE HEADERS

As mentioned before, the first nine bytes of the module header are defined identically for all module types. There is usually more header information immediately following, the layout and meaning varies depending on the specific module type. Module types \$C - \$F are used exclusively by OS-9. Their format is given elsewhere in this manual.

The module type illustrated below is the general-purpose "user" format that is commonly used for OS-9 programs that are called using the FORK or CHAIN system calls. These modules are the "user-defined" types having type codes of Ø through 9. They have six more bytes in their headers defined as follows:

MODULE

DESCRIPTION

- \$9,\$A = Execution Offset. The program or subroutine's starting address, relative to the first byte of the sync code. Modules having multiple entry points (cold start, warm start, etc.) may have a branch table starting at this address.
- \$B.\$C = Permanent Storage Requirement. This is the minimum number of bytes of data storage required to run. This is the number used by FORK and CHAIN to allocate a process data area.

If the module will not be directly executed by a CHAIN or FORK service request (for instance a subroutine package), this entry is not used by OS-9. It is commonly used to specify the maximum stack size required by reentrant subroutine modules. The calling program can check this value to determine if the subroutine has enough stack space.

MODULE OFFSET	EXECUTABLE MEMORY MODULE	FORMAT	
\$ØØ \$Ø1	! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! !		
\$02 \$03	! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! !		
\$Ø4 \$Ø5	! Module Name Offset !	header parity	
\$Ø6	! Type ! Language !		
\$07	! Attributes ! Revision !		! module
\$08	! Header Parity Check !		CRC
\$09 \$0A	Execution Offset		
\$ØB \$ØC	! + Permanent Storage Size		
\$ØD	(Add'1 optional header extensions located here) Module Body object code, constants, etc.		
	CRC Check Value		!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!!

ROMED MEMORY MODULES

When OS-9 starts after a system reset, it searches the entire memory space for ROMed modules. It detects them by looking for the module header syrc code (\$87.\$CD) which are unused 6809 opcodes. When this byte pattern is detected, the header check is performed to verify a correct header. If this test succeeds, the module size is obtained from the header and a 24-bit CRC is performed over the entire module. If the CRC matches correctly, the module is considered valid and it is entered into the module directory. The chances of detecting a false module are virtually nil.

In this manner all ROMed modules present in the system at startup are automatically included in the system module directory. Some of the modules found initially are various parts of OS-9: file managers, device driver, the configuration module, etc.

After the module search OS-9 links to whichever of its component modules that it found. This is the secret of OS-9's extraordinary adaptablity to almost any 6809 computer; it automatically locates its required and optional component modules, wherever they are, and rebuilds the system each time that it is started.

ROMs containing non-system modules are also searched so any user-supplied software is located during the start-up process and entered into the module directory.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL OS-9 System Components

OS-9 SYSTEM COMPONENTS: THE MAJOR MODULES

OS-9 is composed of a number of modules. Some of the component modules are required. Others are optional and included in a particular computer to meet a desired performance level or to support specific hardware or I/O devices. Below is a list of the required and optional modules:

REQUIRED:

Kernel (2K must reside at \$F800-\$FFFF)

Configuration Module

OPTIONAL:

System Bootstrap Module
Input Output Manager (IOMAN)
File Manager (SCFMAN or RBFMAN)

Device Drivers

Device Descriptors

System Initialization Module (SYSGO)

THE KERNEL

The kernel is the heart of OS-9. Its purpose is to:

- 1. Initialize the system after reset
- 2. Process system calls.
- 3. Manage RAM memory.
- 4. Manage the module directory.
- 5. Service interrupts.

The kernel's total size is approximately 3K bytes, which is partitioned into two sections. The first section called "OS9" contains the interrupt vector tables which must be located at \$FFEØ through \$FFFF. The second section called "OS9P2" can be located anywhere in memory. Both sections must be in ROM! If the target system bootstraps other OS-9 component modules into RAM from disk or tape (standard Microware supplied versions do so). OS-9 fits into a 4K ROM or a pair of 2K ROMS, leaving an extra 1K for a bootstrap device driver module. Some lengthy disk driver modules that are larger than 1K require an additional ROM.

THE CONFIGURATION MODULE

This module defines system startup parameters and must also be in ROM (it fits in the same ROM as the kernel). It is a non-executable module named "INIT" and has type "system" (code \$C). It is scanned once during the system startup. It begins with the standard header followed by:

	그는 발표하면 하는 이 회사는 그들은 회사에게 하여 연안 하는데했다.
MODULE OFFSET \$9,\$A,\$B	This location contains the forced limit of free RAM memory. It may be used to cverride OS-9's automatic top-of-RAM search so that memory may be reserved for special purposes.
\$ C	Number of entries to create in the IRQ polling table. One entry is required for each interrupt generating device control register.
\$ D	Number of entries to create in the system device table. One entry is required for each device in the system.
\$E,\$F	Offset to the intitial startup module name string. This is the name of the first module to be executed after startup, usually "SYSGO". There must always be a startup module.
\$10,\$11	Offset to the initial standard path string (typically /TERM). This path is opened as the standard paths for the initial startup module. This location should contain zero if there is none.
\$12,\$13	Offset to the default directory name string (normally /D0). This device is assumed when device names are ommitted from pathlists. If the system will not initialy use a mass storage device (such as in ROM based systems) this location should contain a zero.
	Tell of the second seco

- \$14,\$15 Offset to bootstrap module name string. If OS-9 cannot locate any of its other required modules in ROM, it will call the bootstrap module which attempts to load them from a mass-storage device.
- \$16 to N All name strings go here, followed by three CRC bytes. Name strings must nave the sign bit (bit 7) of the last character set.

(also see the memory module format diagrams in Appendix D)

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL OS-9 System Components

The "default" configuration module included in the OS-9 kernel ROM can be replaced by a user-supplied module. Just make your own module according to the specifications above, giving it a revision level (byte 7 of the header) of one or higher. This module must be in ROM.

THE SYSTEM BOOTSTRAP MODULE

This module loads several of OS-9's required and optional modules from the bootstrap file on a mass-storage device. During the kernel's startup sequence, it tries to link to other modules such as IOMAN. If they are not in memory, the kernel will call the bootstrap module to load them from mass-storage. Some OS-9 systems will have all required modules in ROM so the bootstrap module is not needed. Also see "Running OS-9 As a ROM Based System".

In many systems, it is not cost efffective or otherwise practical to place all of OS-9 into ROM. In these systems, only the kernel, configuration, and bootstrap modules are ROMed. When the system is reset, the kernel will call the bootstrap module which attempts to load the boostrap file into memory from a mass-storage device such as disk or tape. The bootstrap file contains all modules required by OS-9 that are not already in ROM, and other optional modules which should be loaded into memory at startup time. Below is a list of the modules which are typically loaded from the bootstrap file (the actual contents may vary for a particular system or hardware configuration):

MODULE NAME

IOMAN	Input Output Manager
RBF	Random Block File Manager
SCF	Secuential Character File Manager
DSK	Disk Driver (name varies with controller)
ACIA	ACIA Driver
PIA	PIA Driver
Clock	Real-time-clock interface module
DØ	Drive zero device descriptor (DISK)
D1	Drive one device descriptor (DISK)
TERM	Console terminal device descriptor (ACIA)
T1	Secondary terminal device descriptor (ACIA)
T1 P	Printer device descriptor (PIA)
P1	Secondary printer device descriptor (ACIA)
SYSGO	System startup module
SHELL	System command interpreter

OTHER IMPORTANT MODULES

It is possible for very simple computers that don't have standard I/O devices to get by with just the kernel. But most practical systems will be interfaced to a terminal and some kind of mass storage or communications device. Additional OS-9 modules interface the kernel to the I/O system. These are the I/O Manager ("IOMAN"), one or more File Manager modules, one or more Device Driver modules, and one or more Device descriptors.

RUNNING OS-9 AS A ROM BASED SYSTEM

For many aplications using OS-9, a mass storage device such as a disk is either too costly or otherwise inappropriate. Certain applications require that the operating system be functional before a mass-storage device is used; for instance, it may be necessary to be able to run a system diagnostic program such as the interactive DEBUG module before the using the disk drives. OS-9 can eaisly be tailored to meet these needs. To run OS-9 as a ROM based system, all that is required is to adjust two parameters in the system configuration module (INIT) and put a few modules into ROM. Below is a description of how the INIT module must be adjusted:

- A. If the system will not use a mass storage device, set the offset to the default mass-storage device name string to zero in the INIT module.
- B. If it is not necessary to bootstrp from a mass-storage device, set the offset to the BOOT module name string to zero in the INIT module.

Below is a list of the name and approximate size of the modules which are required for OS-9 to start up and be fully functional (complete with the SHELI command interpreter):

MODULE	SIZE	
OS9 OS9P2 IOMAN SCF ACIA TERM SYSGO INIT SHELL	\$76F \$4CB \$646 \$3D9 \$183 \$562 \$403	OS-9 Part One OS-9 Part Two I/O Manager Sequential Character File Manager ACIA device driver Console terminal device descriptor System initialization module System configuration module Shell command line interpreter
	\$1CA1 =	7329 System ROM requirements

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OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL OS-9 System Components

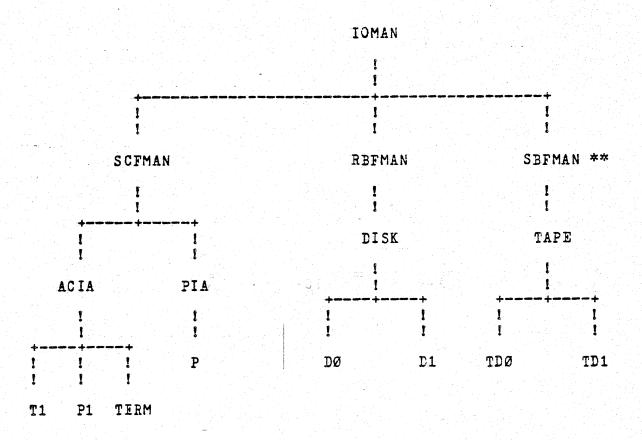
This set of modules is "typical" for a single board microcomputer, the actual system requirements will vary (see the section of this manual on "SYSTEM HARDWARF REQUIREMENTS"). In some applications it may be desirable to include a real-time-clock driver module, bootstrap module (so that a mass-storage device may be used after startup), or the interactive DEBUG module for system diagnostics purposes. The name and size of these modules is given below:

MODULE	SIZE	
CLOCK	\$C7	Real-time-clock driver module
BOOT DEBUG	\$280 \$7BB	Bootstrap module Interactive DEBUG module

NOTE: The actual size of these modules may vary slightly.

THE OS-9 INPUT/OUTPUT SYSTEM

The OS-9 input/output system is constructed from a number of standard or user supplied modules selected to match a computer's hardware configuration. These modules are organized into a hierarchical structure like the one below:



The hierarchical organization makes it extremely easy to reconfigure or expand the I/O system, which is typically done by loading revised or additional modules into memory (they are automatically installed in the I/O system).

At the top level in the diagram, data is device independent and treated as if it were a stream of tytes. Data flowing up from the device level is "filtered" by each module that it passes through, so that when it reaches the top, it will appear as a stream of bytes and no longer be dependent on the way it happened to come into the system. Likewise, data flowing from the top down is "filtered" so that it will conform to any device dependencies; for example, the data may be blocked, line feeds may be inserted after carriage returns, etc.

** NOTE: SEFMAN IS NOT CURRENTLY SUPPORTED BY MICROWARE

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL The OS-9 I/O System

There are four levels of modules in the OS-9 I/O system as given below:

1. I/O Manager (IOMAN)

2. File Managers (SCFMAN, RBFMAN, SBFMAN)

3. Device Drivers (ACIA, PIA, DISK, etc.)

4. Device Descriptors (TERM, DC, D1, etc.)

THE INPUT/OUTPUT MANAGER

The Input/Output Manager (IOMAN) module provides the first level of service for I/O system calls by routing data on I/O paths from processes to/from the appropriate file managers and device drivers. It maintains two important internal OS-9 data structures: the device table and the path table. This module is universal for all OS-9 Level One systems and most users should never need to alter or replace it.

FILE MANAGERS

OS-9 systems can have one or more File Manager modules. The function of a file manager is to process the raw data stream to or from the device drivers to conform to the OS-9 standard file structure.

File managers usually buffer the data stream and issue requests to the kernel for dynamic allocation of buffer memory. They may also monitor and process the data stream, for example, adding line feed characters after carriage return characters.

The file managers are reentrant and one file manager may be used for an entire class of devices having similar operational characteristics. Standard OS-S file managers are:

RBFMAN: The Random Block File Manager which operates random-access, block-structured devices such as disk systems, bubble memories, etc.

SCFMAN: Sequential Character File Manager which is used with single-character-oriented devices such as CRT or hardcopy terminals, printers, modems, etc.

SBFMAN: Sequential Block File Manager which drives block oriented devices that don't have random access capability, mostly tape systems.

NOTE: SBFMAN IS NOT CURRENTLY SUPPORTED BY MICROWARE

Most OS-9 systems will have two file managers: SCFMAN to handle terminals, and either RBFMAN (disk-based systems) or SBFMAN (tape-based systems). It is possible to use all three or more. Sophisticated users may wish to write their own special-purpose file managers.

DEVICE DRIVER MODULES

The device driver modules contain a package of subroutines that perform raw I/O transfers to or from a specific type of I/O device hardware controller. These modules are usually reentrant and one copy of the module can simultaneously run several different devices which use identical I/O controllers. For example the device driver for 6850 serial interfaces is called example the device driver for 6850 serial interfaces is called example the device driver for 6850 serial terminals. "ACIA" and can communicate to any number of serial terminals. Provisions have been made so each "incarnation" of the driver can have its operational characteristics (such as paging, echo, backspace and delete characters, etc.) individually settable.

Device driver modules use a standard module header and are given a module type of "device driver" (code \$\frac{1}{2}\$). The execution offset address in the module header points to a branch table that has a minimum of six (three-byte) entries. Each entry is typically a LBRA to the corresponding subroutine. The File typically a LBRA to the corresponding subroutine through this Managers call specific routines in the device driver through this table, passing a pointer to a "path decriptor" and the hardware table, passing a pointer to a "path decriptor" and the branch table looks like:

- +0 = Device Initialization Routine
- +3 = Read From Device
- +6 = Write to Device
- +9 = Get Device Status
- +\$C = Set Device Status
- +\$F = Device Termination Routine

For a complete description of the parameters passed to these subroutines see the file manager descriptions. Also see the appendicies on writing device drivers.

DEVICE DESCRIPTOR MODULES

These small non-executable modules provide the system with the information it needs to use a device. They associate a specific I/O device with its:

(a) Logical name.

Hardware controller address(es). (b)

(c) Device driver name.

File manager name. (a)

Initial operating parameters. (e)

Recall that device drivers and file managers both operate on classes of devices, not specific devices. The device descriptor modules tailor their functions for a specific I/O device. One device descriptor module must exist for each logical device in the OS-9 environment.

The name of the module is the name the device is known by to the system and user (i.e. it is the device name given in pathlists). Its format consists of a standard module header that has a type "device descriptor" (code \$F). The rest of the device descriptor header consists of:

\$9.\$A = File manager name string relative address.

\$B.\$C = Device driver name string relative address.

\$D = Mode/Capabilities (D S PE PW PR E W R)

\$E,\$F,\$10 = Device controller absolute physical (24-bit) address

\$11 = Number of bytes ("n" bytes in intialization table)

\$12,\$12+n = Initialization table

The initialization table is copied into the "option section" of the path descriptor when a path to the device is opened. values in this table may be used to define the operating parameters that are changeable by the OS9 IsGSTT and IsSSTT parameters that are changeable by the ose initialization service requests. For example, a terminal's initialization parameters define which control characters are used for parameters define which backspace, delete, etc. The maximum size of initialization table which may be used is 32 bytes. If the table is less than 32 bytes long, the remaining values in the path descriptor will be set to zero.

You may wish to add additional devices to your system. If a similar device controller already exists, all you need to do is add the rew hardware and load another device descriptor. Device descriptors can be in ROM or loaded into RAM at any time.

The diagram on the next page illustrates the device descriptor module format.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL The OS-9 I/O System

MODULF OFFS ET	DEVICE DESCRIPTOR MODULE FORMAT		
\$Ø \$1	! + Sync Bytes (\$87CD)+ !		
\$2 \$3	Module Size		
\$4 \$5	! + Offset to Module Name+	! header parity	
\$ 6	! \$F (TYPE) ! \$1 (LANG) !		
\$7	! Atributes ! Revision !		
\$8	! Header Farity Check !		
\$ 9	1 Officet to File Manager		
\$A	+ Offset to File Manager+ ! Name String !		module CRC
\$B \$C	! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! !		
\$ D	! Mode Byte !		
\$E \$F \$10	Device Controller Absolute Physical Address (24 bit)		
\$11	! Initialization Table Size !		
\$12,\$12+N	! (Initialization Table) !		
	(Name Strings etc)		
	CRC Check Value		

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Below are the initialization table definitions for SCF and RBF type devices:

SCF DEVICE INITIALIZATION TABLE

MODULE OFFS ET	ORG	\$12	
\$13456789ABCDEFØ12345678AC	IT.DEL RM IT.EOR RM IT.EOF RM IT.RPR RM IT.DUP RM IT.PSC RM IT.INT RM IT.OUT RM IT.OUT RM IT.BSE RM IT.OVF RM IT.PAR RM IT.BAU RM IT.BAU RM IT.BAU RM IT.BAU RM	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	BEGINING OF OPTION TABLE DEVICE CLASS (@=SCF 1=RBF 2=PIPE 3=SBF) CASE (@=BOTH, 1=UPPER ONLY) BACK SPACE (@=BSE, 1=BSE,SP,BSE) DELETE (@=BSE OVER LINE, 1=CR) ECHO (@=NO ECHO) AUTO LINE FEED (@= NO AUTO LF) END OF LINE NULL COUNT PAUSE (@= NO END OF PAGE PAUSE) LINES PER PAGE BACKSPACE CHARACTER DELETE LINE CHARACTER END OF RECORD CHARACTER END OF FILE CHARACTER REPRINT LINE CHARACTER PAUSE CHARACTER OUP LAST LINE CHARACTER INTERRUPT CHARACTER UP LAST LINE CHARACTER UNTERRUPT CHARACTER LINE OVERFLOW CHARACTER LINE OVERFLOW CHARACTER (BELL) INITIALIZATION VALUE (PARITY) BAUD RATE ATTACHED DEVICE NAME STING OFFSET RESERVED INTITIAL ERROR STATUS

NOTES:

SCF editing functions will be "turned off" if the corresponding special character is a zero. For example, if IT.EOF was a zero, there would be no end of file character. For a full description, please see the section of this manual on Sequential Character File Manager".

IT.PAR is typically used to intitialize the device's control register when a path is opened to it.

OS-9 LEVEL CNE SYSTEM PROGRAMMER'S MANUAL The OS-9 I/O System

RRF DEVICE INITIALIZATION TABLE

MODULE OFFSET	ORG	\$12
\$12 \$13 \$14 \$15 \$17 \$110 \$110 \$110 \$120	IT.DTP RMB IT.DRV RMB IT.STP RMB IT.TYP RMB IT.DNS RMB IT.CYL RMB IT.SID RMB IT.SID RMB IT.YFY RMB IT.SCT RMB IT.SCT RMB IT.SCT RMB IT.SCT RMB IT.SCT RMB	DRIVE NUMBER DRIVE NUMBER STEP RATE DEVICE TYPE (See RBFMAN path descriptor) MEDIA DENSITY (Ø = SINGLE, 1=DOUBLE) NUMBER OF CYLINDERS (TRACKS) NUMBER OF SURFACES (SIDES) Ø = VERIFY DISK WRITES Default Sectors/Track Default Sectors/Track (Track Ø) SECTOR INTERLEAVE FACTOR

NOTES:

IT.DRV This location is used to associate a one byte integer with each drive that a controller will handle. The drives for each controller should be numbered Ø to N-1.

IT.STP This location sets the head stepping rate that will be used with a drive. The step rate should be set to the fastest value that the drive is capable of to reduce access time. Below are the values which should be used:

	+=====================================	! FD1791,	FD1797 !
STEP	+=====================================	1 5"	8"!
Ø	! 40ms ! 20ms	! 30ms	15ms !
1	! 20 ms ! 10 ms	1 20ms	10ms !
2	! 12ms ! 6ms	! 12ms	6ms !
3	! 12ms ! 6ms	! 6ms	3ms 1
			· ·

The DC-2, DC-3, GMX #58 and BFD-68A controllers use the FD1771 type chips.

The DF-2, GMX #28, GMX DMA, and DCB-4 controllers use the FD1791 or FD1797 type chips.

IT.TYP Device type.

bit
$$\emptyset -- \emptyset = 5$$
 floppy disk
 $1 = 8$ floppy disk

IT.DNS Density capabilities

bit
$$\emptyset$$
 -- \emptyset = Single bit density
1 = Double bit density

IT.SAS This value specifies the minimum number of sectors to be allocated at any one time.

For more information, please the section of this manual on RBFMAN definitions of the path descriptor.

OS-9 LEVEL CNE SYSTEM PROGRAMMER'S MANUAL The OS-S I/O System

PATH DESCRIPTORS

Every open path is associated with a data structure called a path descriptor ("PD"). It contains the information required by the file managers and device drivers to perform I/O functions. Path descriptors are exactly 64 bytes long and are dynamically allocated and deallocated by IOMAN in response to requests from user programs.

PDs are INTERNAL data structures that are not normally referenced from user or applications programs. In fact, it is almost impossible to locate a path's PD when OS-9 is in user mode. The description of PDs is mostly of interest to, and presented here for those programmers who need to write custom file managers, device drivers, or other extensions to OS-9.

PDs have three sections: the first 10-byte section is defined universally for all file managers and device drivers. The second 22-byte section is reserved for and defined by each type of file manager. The last 32-byte section is used as an "option" area for communications with user programs via the GETSTAT and SETSTAT system calls. The variables in this area are typically used for dynamically-alterable operating parameters for the file or device.

Universal Path Descriptor Definitions

Name .	Addr Size	Description
PD.PD	\$00 1	Path number
	\$01 1	Access mode: 1=read 2=write 3=update
PD.CNT	\$02 1	Number of paths using this PD
PD.DEV	\$03 2	Address of associated device table entry
PD.CPR	95 1	Requester's process ID
PD.RGS	96 2	Caller's MPU register stack address
PD.BUF	\$08 2	Address of 256-byte data buffer (if used)
PD.FST	0A 22	Reserved for file manager
PD.OPT	20 32	Reserved for GETSTAT/SETSTAT options

For more information, please see the sections of this manual on file managers, device descriptors, and writing device drivers.

RANDOM BLOCK FILE MANAGER

The Random Block File Manager (RBFMAN) is the OS-9 module that supports random-access, block-oriented devices such as disk systems, bubble memory systems, and high-performance tape systems. RBFMAN can handle any number or type of such systems simultaneously. It is a reentrant subroutine package called by IOMAN for I/C service requests to random-access devices. It is responsible for maintaining the logical and physical file structures.

In the course of normal operation, RBFMAN requests allocation and deallocation of 256-byte data buffers; usually one is required for each open file. When physical I/O functions are necessary, RBFMAN directly calls the subroutines in the associated device drivers. All data transfers are performed using 256-byte "blocks". RBFMAN does not know about tracks, cylinders, etc. Instead, it passes the drivers a "logical sector number ranging from 0 to n-1, where n is the maximum number of sectors on the media. The driver is responsible for translating the logical sector number to actual cylinder/track/sector values.

Because RBFMAN is designed to support a wide range of devices having different performance and storage capacity, it is highly parameter-driven. The physical parameters it uses are stored on the media itself. On disk systems, this information is written on the first few sectors of track number zero. The device drivers also use this information, particularly the physical parameters stored on sector Ø. These parameters are written by the "format" program that initializes and tests the media.

NOTE: IOMAN allocates a 64 byte path descriptor when a path is opened or created, and deallocates the path descriptor after the path is closed.

LCGICAL AND PHYSICAL DISK ORGANIZATION

Identification Sector

Logical sector number zero contains a description of the physical and logical characteristics of the volume. These are established by the "format" command program when the media is initialized. the table below gives the OS-9 mnemomic name, byte address, size, and description of each value stored in this sector.

name a	ddr size	description
DD.TOT \$	00 3	Total number of sectors on media
	Ø3 1	Number of sectors per track
	04 2	Number of bytes in allocation map
	96 2	Number of sectors per cluster
	08 3	Starting sector of root directory
	ØB 2	Owner's user number
	ØD 1	Disk attributes
,	ØF 2	Disk identification (for internal use)
	10 1	Disk format: density, number of sides
	11 2	Number of sectors per track.
	13 2	Reserved for future use
DD.BT		Starting sector of bootstrap file
	18 2	Size of bootstrap file (in bytes)
DD.DAT		Time of creation: Y:M:D:H:M
	1F 32	Volume name: last char has sign bit set

Disk Allocation Map Sector ;

One sector of the disk is used for the "disk allocation map" that specifies which clusters on the disk are available for allocation of file storage space. The address of this sector is always assigned logical sector 1 by the format program. DD.MAP specifies the number of bytes in this sector which are actually used in the map.

Each bit in the map corresponds to a cluster of sectors on the disk. The number of sectors per cluster is specified by the "DD.BIT" variable in the identification sector, and is always an integral power of two, i.e., 1, 2, 4, 8, 16, etc. There are a maximum of 4096 bits in the map, so media such as double-density double-sided floppy disks and hard disks will use a cluster size of two or more sectors. Each bit is cleared if the corresponding cluster is available for allocation, or set if the sector is already allocated, non-existent, or physically defective. The bitmap is initially built by the format program.

File Descriptor Sectors

The first sector of every file is called a "file descriptor", which contains the logical and physical description of the file. The table below describes the contents of the descriptor.

name	addr	size	descript	tion				
Company and pay are see the								
FD.ATT FD.OWN FD.DAT FD.LNK FD.SIZ FD.DCR	91389D	1 2 5 1 4 3	Owner Date Link File	Attribute r's User I Last Modi Count Size (nur Created:	ID fied: nber of	Y M D	H M	e w R
FD.SEG	\$10	240	Segm	ent List:	see be	low		

The attribute byte contains the file permission bits. Bit 7 is set to indicate a directory file, bit 6 indicates a "sharable" file, bit 5 is public execute, bit 4 is public write, etc.

The segment list consists of up to 48 five-byte entries that have the size and address of each block of storage that comprise the file in logical order. Fach entry has a three-byte logical sector number of the block, and a two-byte block size (in sectors). The entry following the last segment will be zero.

When a file is created, it initially has no data segments allocated to it. Write operations past the current end-of-file (the first write is always past the end-of-file) cause additional sectors to be allocated to the file. If the file has no segments, it is given an initial segment having the number of sectors specified by the minimum allocation entry in the device descriptor, or the number of sectors requested if greater than the minimum. Subsequent expansions of the file are also generally made in minimum allocation increments. An attempt is made to expand the last segment wherever possible rather than adding a new segment. When the file is closed, unused sectors in the last segment are truncated.

A note about disk allocation: OS-9 attempts to minimize the number of storage segments used in a file. In fact, many files will only have one segment in which case no extra read operations are needed to randomly access any byte on the file. Files can have multiple segments if the free space of the disk becomes very fragmented, or if a file is repeatedly closed, then opened and expanded at some later time. This can be avoided by writing a byte at the highest address to be used on a file before writing any other data.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL The OS-9 I/O System - RBFMAN

Directories

Disk directories are files that have the "D" bit set in their attribute byte. Each directory entry is 32 bytes long, consisting of 29 bytes for the file name followed by a three byte logical sector number of the file's descriptor sector. The file name is left-justified in the field with the sign bit of the last character set. Unused entries have a zero byte in the first file name character position. Every mass-storage media must have at least one master directory called the "root directory". The beginning logical sector number of this directory is stored in the identification sector, as previously described.

RBFMAN Definitions of the Path Descriptor

The table below describes the usage of the file-managerreserved section of rath descriptors used by RBFMAN. Also see the section of this manual on path descriptors.

Name	Addr	Size	Description
Univers	al Sect	ion	
PD.PD PD.MOD PD.CNT PD.DEV PD.CPR PD.RGS PD.BUF	\$02 \$03 \$05 \$06	1 1 1 2 1 2 2 2	Path number Mode (read/write/update) Number of open images Address of device table entry Current process ID Address of callers register stack Buffer address
RBFMAN	Path De	scriptor	Definitions
PD.SMF PD.CP PD.SIZ PD.SBL PD.SBP PD.SSZ PD.DSK PD.DTB	\$0B \$0F \$13 \$19 \$1B	1 4 4 3 3 2 2 2	State flags (see next page) Current logical file position (byte addr) File size Segment beginning logical sector number Segment beginning physical sector number Segment size Lisk ID (for internal use only) Address of drive table

(continued)

RBFMAN Option Section Definitions

```
Device class 0= SCF 1=RBF 2=PIPE 3=SBF
        $20
                        Drive number (0...N)
        $21
               1
PD.DRV
                        Step rate
        $22
               1
PD.STP
        $23
               1
                        Device type
PD.TYP
                        Density capability
        $24
               1
PD.DNS
                        Number of cylinders (tracks)
               2
        $25
PD.CYL
                        Number of sides (surfaces)
               1
PD.SID
        $27
                        Ø = verify disk writes
               1
PD.VFY
        $28
                        Default number of sectors/track
               2
PD.SCT
        $29
                        Default number of sectors/track (track 0)
               2
        $2B
PD.TES
                        Sector intreleave factor
               1
        $2D
PD.ILV
                        Segment allocation size
        $2I
               1
PD.SAS
```

(the following values are NOT copied from the device descriptor)

```
File attributes (D S PE PW PR E W R)
PD.ATT
        $33
                       File descriptor PSN (physical sector #)
        $34
               3
PD.FD
                       Directory file descriptor PSN
               3
        $37
PD.DFD
                       File's directory entry pointer
        $3A
PD.DCP
                       Address of device table entry
               2
PD.DVT
        $3E
```

State Flag (PD.SMF): the bits of this byte are defined as:
bit C = set if current buffer has been altered
bit 1 = set if current sector is in buffer
bit 2 = set if descriptor sector in buffer

The first section is universal for all file managers, the second and third sections are specific for RBFMAN and RBFMAN-type device drivers. The option section of the path descriptor contains many device operating parameters which may be read and/or written by the OSS ISGSTT and ISSSTT service requests. This section is initialized by IOMAN who copies the initialization table of the device descriptor into the option section of the path descriptor when a path to a device is opened. Any values not determined by this table will default to zero.

NOTE: For a description of the values copied into the option section of the path descriptor and other related information, please see the section of this manual on device descriptors.

SEQUENTIAL CHARACTER FILE MANAGER

The Sequential Character File Manager (SCFMAN) is the OS-9 module that supports character-oriented devices such as terminals, printers, modems, etc. SCFMAN can handle any number or type of such systems. It is a reentrant subroutine package called by IOMAN for I/O service requests to sequential character oriented devices. It includes the extensive input and output editing functions typical of line-oriented operation such as: backspace, line delete, repeat line, auto line feed, screen pause, return delay padding, etc.

SCFMAN Definitions of The Path Descriptor

The table below describes the path descriptors used by SCFMAN and SCFMAN-type device drivers. Also see the section of this manual on path descriptors and device descriptors.

Name	Offse	t Size	Description
Univers	al Se	ction	
PD.CPR PD.RGS PD.BUF	\$02 \$05 \$05 \$08	1 1 2 1 2 2 2	Path number Mode (read/write/update) Number of open images Address of device table entry Current process ID Address of callers MPU register stack Euffer address Definitions
PD.DV2 PD.RAW PD.MAX PD.MIN	\$ØA \$ØC \$ØD \$ØF	2 1 2 1	Device table addr of 2nd (echc) device Edit flag: Ø=raw mode, 1=edit mode Readline maximum character count Devices are "mine" if cleared

(continued)

SCFMAN Option Section Definition

	\$20	1	Device class Ø=SCF 1=RBF 2=PIPE 3=SBF
PD.UPC	\$21	1	Case (0=BOTH, 1=UPPER ONLY)
PD.BSC		1	Backsp (@=BSE, 1=BSE SP BSE)
PD.DLO		$\mathbf{i} = \mathbf{j} 1$	Delete (@ = BSE over line, 1=CR LF)
PD.EKO		1	Echo (0=no echo)
PD.ALF	\$25	1	Auto LF (0=no auto LF)
PD.NUI		1	End of line null count
PD.PAU		1	Pause (0= no end of page pause)
PD.PAG		1	Lines per page
PD.BSP		1 1	Backspace character
PD.DEL	\$2A	1	Delete line character
PD.EOR	2 -	1	End of record character (read only)
PD.EOF		Ī	End of file character (read only)
PD.RPR	and the second second	1	Reprint line character
PD.DUP			Duplicate last line character
PD.PSC		ī	Pause character
PD.INT		ī	Keyboard interrupt character (CTL C)
PD.CUT			Keyboard abort character (CTL Q)
PD.BSE			Backspace echo character (BSE)
PD.OVF			Line overflow character (bell)
PD.PAR		1	Device initialization value (parity)
PD.BAU	\$35	$ar{f 1}$	Software settable baud rate
I D . DAG	700		화매하기 많아. 하는데 나는 그 살아 보는 것이다.
PD.D2P	\$36	2	Offset to 2nd device name string
ID-DEI	\$38		Reserved for future use
PD.ERR		1	Accumulated I/O error status.
PD.TBL	\$3B	2	Address of device table
The The	7.00	~	

The first section is universal for all file managers, the second and third section are specific for SCFMAN and SCFMAN-type device drivers. The option section of the path descriptor contains many device operating parameters which may be read or written by the OS9 I\$GSTT or I\$SSTT service requests. IOMAN initializes this section when a path is opened to a device by copying the corresponding device descriptor initialization table. Any values not determined by this table will default to zero.

Special editing functions may be disabled by setting the corresponding control character value to zero.

SCFMAN Line Editing Features

ISREAD and ISWRITE service requests to SCFMAN type devices generally pass the data to/from the device without any modification, except that keyboard interrupt, keyboard abort, and pause character are filtered out of the input (editing is disabled if the corresponding character in the path descriptor contains a zero). In particular carriage returns are not automatically followed by line feeds or nulls, and the high order bits are passed as sent/received.

I\$RDLN and I\$WRLN service requests to SCFMAN type devices will cause the following editing to occur:

All bytes input or output have their high order bit cleared.

If PD.UPC <> 0 bytes input or output in the range "a..z" are made "A..Z"

If PD.EKO <> Ø, input bytes are echoed, except that undefined control characters in the range \$0..\$1F print as

If PD.ALF <> 0, carriage returns are automatically followed by line feeds.

If PD.NUL <> Ø, After each CR/LF a PD.NUL "nulls" (always \$00) are sent.

If PD.PAU <> 0, Auto page pause will occur after every PD.PAU lines since the last input.

If PD.BSP <> Ø, SCF will recognize PD.BSP as the "input" backspace character, and will echo PD.BSE (the backspace echo character) if PD.BSO = Ø, or PD.BSE, space, PD.BSE if PD.BSO <> Ø.

If PD.DEL <> Ø, SCF will recognize PD.DEL the delete line character (on input), and echo the "backspace sequence over the entire line if PD.DLO = Ø, or echo CR/LF if PD.DLO <> Ø

PD.EOR defines the end of record character. This is the last character on each line entered (I\$RDLN), and terminates the output (I\$WRLN) when this character is sent. Normally PD.EOR will be set to \$ØD. If it is set to zero, SCF's READLN will NEVER terminate, unless an EOF occurs.

If PD.EOF <> 0. it defines the end of file character. SCFMAN will return an end-cf-file error on I\$READ or I\$RDLN if this is the first (and only) character input. It can be disabled by setting its value to zero.

If PD.RPR <> 0. SCF (I\$RDLN) will, upon receipt of this character, echo a carriage return [optional line feed], and then reprint the current line.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL The OS-9 I/O System - SCFMAN

If PD.DUP <> 0. SCF (ISRDLN) will duplicate whatever is in the input buffer through the first "PD.EOR" character.

If PD.PSC <> 0. output is suspened before the next "PD.FOR" character when this character is input. This will also delete any "type ahead" input for I\$RDLN.

If PD.INT <> 0, and it is received on input, a keyboard interrupt signal is sent to the last user of this path. Also it will terminate the current I/O request (if any) with an error identical to the keyboard interrupt signal code. This location normally is set to a control-C character.

If PD.QUT <> 0, and it is received on input, a keyboard abort signal is sent to the last user of this path. Also it will terminate the current I/O request (if any) with an error code identical to the keyboard interrupt signal code. This location is normally set to a control-Q character.

If PD.OVF <> 0, It is echoed when I\$RDLN has satisfied its input byte count without finding a PD.EOR character.

NOTE: It is possible to disable most of these special editing functions by setting the corresponding control character in the path descriptor to zero by using the I\$SSTT service request. A more "permanent" solution may be had by setting the corresponding control character value in the device descriptor to zero.

Device descriptors may be inspected to determine the default settings for these values.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Interrupt Processing

INTERRUPT PROCESSING

The OS-9 kernel ROMS contain the hardware vectors required by the 6809 MPU at addresses \$FFF0 through \$FFFF. These vectors each point to jump-extended-indirect instruction which vector the MPU to the actual interrupt service routine. A RAM vector table in page zero of memory contains the target addresses of the jump instructions as follows:

INTERRUPT	ADDRESS
SWI3	\$0020
SWI2	\$002E
FIRO	\$0030
IRC	\$0032
SWI	\$0034
NMI	\$0036

OS-9 initializes each of these locations after reset to point to a specific service routine in the kernel. The SWI, SWI2, and SWI3 vectors point to specific routines which in turn read the corresponding pseudo vector from the process' process descriptor and dispatch to it. This is why the F\$SSWI service request to be local to a process since it only changes a pseudo vector in the process descriptor. The IRQ routine points directly to the IRQ polling system, or to it indirectly via the real-time clock device service routine. The FIRQ and NMI vectors are not normally used by OS-9 and point to RTI instructions.

A secondary vector table located at \$FFEØ contains the addresses of the routines that the RAM vectors are initialized to. They may be used when it is necessary to restore the original service routines after altering the RAM vectors. On the next page are the definitions of both the actual hardware interrupt vector table, and the secondary vector table:

OS-9 Interrupt Vector Tables

VECTOR ADDRESS

OS-9 Secondary Vector Table

TICK	\$FFE@	Clock Tick Service Routine
SWI3	\$FFE2	
SWIZ	\$FFE4	
FIRQ	\$FFE6	
IRQ	\$FFE8-	
SWI	\$FFE A	
NMI	SFFEC	
WARM	SFFEE	Reserved for warm-start

Actual Hardware Vector Table

SWI3	\$FFF2
SWI2	\$FFF4
FIRQ	\$FFF6
IRQ	\$FFF8
SWI	\$FFFA
NMI	\$FFFC
RESTART	SFFFE

If it is necessary to alter the RAM vectors use the secondary vector table to exit the substitute routine. The technique of altering the IRQ pointer is usually used by the clock service routines to reduce latency time of this frequent interrupt source.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Interrupt Processing

IRQ AUTOMATIC POLLING SYSTEM

In OS-9 systems, most I/O devices use IRQ-type interrupts, so OS-9 includes a sophisticated polling system that automatically identifies the source of the interrupt and dispatches to its associated user-defined service routine. The information required for IRQ polling is maintained in a data structure called the "IRQ polling table". The table has a 9-byte entry for each possible IRQ-generating device. The table size is static and defined by an initialization constant in the System Configuration Module.

The polling system is prioritized so devices having a relatively greater importance (i.e., interrupt frequency) are polled before those of lesser priority. This is accomplished by keeping the entries sorted by priority, which is a number between Ø (lowest) and 255 (highest). Each entry in the table has 6 variables:

- 1. POLLING ADDRESS: The address of the device's status register, which must have a bit or bits that indicate it is the source of an interrupt.
- 2. MASK BYTE: This byte selects one or more bits within the device status register that are interrupt reguest flag(s). A set bit identifies the active bit(s).
- 3. FLIP PYTE: This byte selects whether the bits in the device status register are true when set or true when cleared. Cleared bits indicate active when set.
- 4. SERVICE ROUTINE ADDRESS: The user-supplied address of the device's interrupt service routine.
- 5. STATIC STORAGE ADDRESS: a user-supplied pointer to the permanent storage required by the device service routine.
- 6. PRIORITY: The device priority number: Ø to 255. This value determines the order in which the devices in the polling table will be polled. Note: this is not the same as a process priority which is used by the execution scheduler to decide which process gets the next time slice for MPU execution.

when an IRQ interrupt occurs, the polling system is entered via the corresponding RAM interrupt vector. It starts polling the devices, using the entries in the polling table in priority order. For each entry, the status register address is loaded into accumulator A using the device address from the table. An

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Interrupt Processing

exclusive-or operation using the "flip-byte" is executed, followed by a logical-and operation using the mask byte. If the result is non-zero, the device is assumed to be the cause of the interrupt. The device's static storage address and service routine address is read from the table, and jumped to.

NOTE: The interrupt service routine should terminate with an RTS - NOT AN RTI instruction.

Entries can be made to the IRQ polling table by use of a special OS-9 service request called "F\$IRQ". This is a priviledged service request that can be executed only when OS-9 is in System Mode (which is the case when device drivers are executed).

WRITING INTERRUPT-DRIVEN DEVICE DRIVERS

It is important to understand that interrupt service routines are asynchronous and somewhat nebulous in that they are not distinct processes. In fact, when they are invoked ANY indeterminate process may have been interrupted, but not necessarily the process that triggered the interrupt-causing event.

Therefore, all interrupt-driven device drivers have two basic parts: the "mainline" subroutines that execute as part of the calling process, and a separate interrupt service routine.

THE TWO ROUTINES ARE ASYNCHRONOUS AND THEREFORE MUST USE SIGNALS FOR COMMUNICATIONS AND COORDINATION.

The INIT initialization subroutine within the driver package should allocate static storage for the service routine, get the service routine address, and execute the F\$IRQ system call to add it to the IRQ polling table.

When a device driver routine does something that will result in an interrupt, it should immediately execute a F\$SLEP service request. This results in the process' deactivation. When the interrupt in question occurs, its service routine is executed after some random interval. It should then do the minimal amount of processing required, and send a "wakeup" signal to its associated process using the F\$SEND service request. It may also put some data in its static storage (I/O data and status) which is shared with its associated "sleeping" process.

Some time later, the device driver "mainline" routine is awakened by the signal, and can process the data or status returned by the interrupt service routine. Remember that processes that execute "sleep" requests while in system state are given maximum priority by the scheduler.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Writing Assembly Language Programs

WRITING CS-9 ASSEMBLY LANGUAGE PROGRAMS

There are four key rules for programmers writing OS-9 assembly language programs:

- 1. All programs MUST use position-independent-code (PIC). OS-9 selects load addresses based on available memory at run-time. There is no way to force a program to be loaded at a specific address.
- 2. Programs must be organized as contiguous memory modules, which are the OS-9 equivalent of load records.
- 3. Storage for variables and data structures must be part of the data area which is assigned by OS-9 at run-time, and is separate from the program module (section).
- 4. All input and output operations should be made using OS-9 service request calls.

Fortunately, the 6809's versatile addressing modes make the rules above easy to follow. The OS-9 assembler also helps because it has special capabilities to assist the programmer in creating programs for the OS-9 execution environment.

Using Position-Independent Code

It is simple to write 6809 Position-independent-code (PIC). The trick is to always use PC-relative addressing; for example BRA, LBRA, BSR and LBSR. Get addresses of constants and tables using LEA instructions instead of load immediate instructions. If you use dispatch tables, use tables of RELATIVE, not absolute, addresses.

INCORRECT

CORRECT

LDX #TABLE

LEAX TABLE, PCR

JSR SUBR

BSR SUBR or LBSR SUBR

Standard Input/Output Paths

Programs should be written to use standard I/O paths #0 (input), #1 (output), and #2 (error output) wherever practical. Usually, this involves I/O calls that are intended to communicate to the user's terminal, or any other case where the OS-9 redirected I/O capability is desirable. These paths do not have to be OPENed or CLOSEd.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Writing Assembly Language Programs

How to Select Addressing Modes

Programs to be invoked using FORK and CHAIN system calls have RAM memory assigned at execution-time. The addresses cannot be known or specified ahead of time. Again, thanks to the 6809's full compliment of addressing modes this presents no problem.

When the program is first entered, the Y register will have the address of the top of your data memory area. If the creating process passed a parameter area, it will be located from the value of the SP to the top of memory (Y), and the D register will contain the parameter area size in bytes. If the new process was called by the shell, the parameter area will contain the part of the shell command line that includes the argument (parameter) text. The U register will have the lower bound of the data memory area, and the DP register will contain its page number.

The most important rule is to NOT USE EXTENDED ADDRESSING! Indexed and direct page addressing should be used exclusively to access data area values and structures. Do not use program-counter relative addressing to find addresses in the data area, but do use it to refer to addresses within the program area.

The most efficient way to handle tables, buffers, stacks, etc., is to have the program's initialization routine compute their absolute addresses using the data area bounds passed by OS-9 in the registers. These addresses can then be saved in the direct page where they can be loaded into registers quickly, using short instructions.

This technique has advantages: it is faster than extended addressing, and the program is inherently reentrant.

Machine Stack Requirements

Because OS-9 uses interrupts extensively, and also because many reentrant 6809 programs use the MPU stack for local variable storage, a generous stack should be maintained at all times. The recommended minimum is approximately 200 bytes.

Interrupt Masks

User programs should keep the condition codes register F (FIRC mask) and I (IRQ mask) hits off. They can be set during critical program sequences to avoid task-switching or interrupts, but this time should be kept to a mimimum. If they are set for longer than a tick period, system timekeeping accuracy will be affected.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Writing Assembly Language Programs

Example Program

The example program shown below is presented here to provide some idea as to how addressing modes etc. are actually used. This program will print "HELIO WORLD" on the terminal, and then wait for a line to be typed in.

NAM EXAMPLE
OPT -M
USE /DØ/DEFS/OS9DEFS

*
* OS-9 System Definition File Included

Ø ORG SAVE LINE LENGTH HERE 2 RMB LINLEN LINE INPUT BUFFER 80 RMB INPBUF MINIMUM HARDWARE STACK SIZE 200 RMB .-1 EQU STACK DATA AREA MEMORY SIZE EQU DATMEM

MOD ENDPGM, NAME, OBJCT+PRGRM, REENT+1, ENTRY, DATMEM

MODULE NAME STRING /EXAMPLE/ FCS -NAME MODULE EXECUTION ENTRY POINT EQU ENTRY GET ADDRESS OF OUTPUT STRING OUTSTR, PCR LEAX GET STRING LENGTH LDY #STRLEN GET STANDARD OUTPUT PATH NUMB LIA #1 WRITE THE LINE ISWRLN 059 BRA IF ANY I/O ERRORS OCCURED ERROR BCS GET ADDR OF INPUT BUFFER INPBUF,U LEAX INPUT MAX OF 80 CHARACTERS LDY #80 GET STANDARD INPUT PATH NUMBE #0 LDA READ THE LINE INTO THE BUFFER I \$RDLN 059 BRA IF ANY I/O ERRORS OCCURED ERROR BCS SAVE THE LINE LENGTH STY LINLEN RETURN WITH NO ERRORS CLRE TERMINATE THE PROCESS FSEXIT 089 ERROR

OUTSTR FCC /HELLO WORLD/ OUTPUT STRING FCB \$ØD END OF LINE CHARACTER

STRLEN EQU *-OUTSTR STRING LENGTH

EMOD END OF MODULE END OF PROGRAM

NOTE: The OS9DEFS system definitions file is supplied with the OS-9 assembler. Also this file may be located in a different directory than the one given in the "USE" statement in the program.

OS-9 SERVICE REQUEST DESCRIPTIONS

System calls are used to communicate between the OS-9 operating system and assembly-language-level programs. There are three general categories:

- 1. User mode function requests
- 2. System mode function requests
- 3. I/O requests

System mode fuction requests are priviledged and may be executed only while OS-9 is in the system state (when it is processing another service request). They are included in this manual primarily for the benefit of those programmers who will be writing device drivers and other sophisticated applications.

The system calls are performed by loading the MPU registers with the appropriate parameters (if any), and executing a SWI2 instruction immediately followed by a constant byte which is the request code. Parameters (if any) will be returned in the MPU registers after OS-9 has processed the service request. A standard convention for reporting errors is used in all system calls; if an error occured, the "C bit" of the condition code register will be set and accumulator B will contain the appropriate error code. This permits a BCS or BCC instruction immediately following the system call to branch on error/no error.

Here's an example system call for the "CLOSE" service request (code \$8B):

LDA PATHNUM SWIZ FCE \$8B BCS ERROR

Using the assembler's "OS9" directive simplifies the call:

LDA PATHNUM OS9 ISCLOS BCS ERROR

OS-9 LEVEL CNE SYSTEM PROGRAMMER S MANUAL Service Request Descriptions

The I/O service requests are simpler to use than in many other operating systems because the calling program does not have to allocate and set up "file control blocks", "sector buffers", etc. Instead OS-9 will return a one byte path number when a path to a file/device is opened or created; then this path number may be used in subsequent I/O requests to identify the file/device until the path is closed. OS-9 internally allocates and maintains its own data structures and users never have to deal with them: in fact attempts to do so are memory violations.

All system calls have a mnemonic name that starts with "F\$" for system functions, or "I\$" for I/O related requests. These are defined in the assembler-input equate file called "OS9DEFS".

In the service request descriptions which follow, registers not explicitly specified as input or output parameters are not altered.

Strings passed as parameters are normally terminated by having bit seven of the last character set, a space character, or an end of line character.

ABIT

Set bits in an allocation bit map

F\$ABIT

ASSEMBLER CALL: OS9 F\$ABIT

MACHINE CODE: 103F 13

(X) = Base address of allocation bit map.(D) = Bit number of first bit to set. INPUT:

(Y) = Bit count (number of bits to set).

OUTPUT: None.

FRROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system mode service request sets bits in the allocation bit map specified by the X register.

Bit numbers range from Ø..N-1, where N is the number of bits in the allocation bit map.

CHAIN Load and execute a new primary module.

F\$CHAN

ASSEMBLER CALL: OS9 F\$CHAN

MACHINE CODE: 103F 05

INPUT: (X) = Address of module name or file name.

(Y) = Parameter area size (256 byte pages).(U) = Beginning address of parameter area.

(A) = Language / type code.

(B) = Optional data area size (256 byte pages).

ERROR OUPTPUT: (CC) = C bit set.

(E) = Appropriate error code.

This system call is similar to FORK, but it does not create a new process. It effectively "resets" the calling process' program and data memory areas and begins execution of a new primary module. Open paths are not closed or otherwise affected.

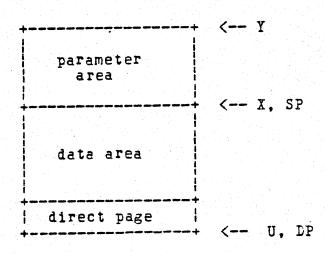
This system call is used when it is necessary to execute an entirely new program, but without the overhead of creating a new process. It is functionally similar to a FORK followed by an EXIT. but with less processing overhead.

The sequence of operations taken by CHAIN is as follows:

- The system parses the name string of the new process' primary module the program that will initially be executed. Then the system module directory is searched to see if a module with the same name and type / language is already in memory. If so it is linked to. If not, the name string is used as the pathlist of a file which is to be loaded into memory. Then the first module in this file is linked to (several modules may have been loaded from a single file).
- 2. The process' old primary module is UNLINKED.
- 3. The data memory area is reconfigured to the size specified in the new primary module's header.

CHAIN (continued)

The diagram below shows how CHAIN sets up the data memory area and registers for the new module.



D = parameter area size

PC = module entry point abs. address

CC = F=0, I=0, others undefined

Y (top of memory pointer) and U (bottom of memory pointer) will always have a values at 256-byte page boundaries. If the parent does not specify a parameter area, Y, X, and SP will be the same, and D will equal zero. The minimum overall data area size is one page (256 bytes).

WARNING: The hardware stack pointer (SP) should be located somewhere in the direct page before the F\$CHAN service request is executed to prevent a suicide attempt error or an acutal suicide (system crash). This will prevent a suicide from occuring in case the new module requires a smaller data area than what is currently being used. You should allow approximately 200 bytes of stack space for execution of the F\$CHAN service request and other system overhead.

For more information, please see the F\$FORK service request description.

COMPARE NAMES Compare two names.

F\$CNAM

ASSEMBLER CALL: OS9 FSCNAM

MACHINE CODE: 103F 11

INPUT: (X) = Address of first name.

(B) = Length of first name.(Y) = Address of second name.

OUTPUT: (CC) = C bit clear if the strings match.

Given the address and length of a string, and the address of a second string, compares them and indicates whether they match.

Typically used in conjunction with "parsename".

The second name must have the sign bit (bit 7) of the last character set.

CRC

Compute CRC

F\$CRC

ASSEMBLER CALL: OS9 F\$CRC

MACHINE CODE: 103F 17

INPUT: (X) = Starting byte address.

(Y) = Byte count.

(U) = Address of 3 byte CRC accumulator.

OUTPUT: CRC accumulator is updated.

ERROR OUTPUT: None.

This service request calculates the CRC (cyclic redundancy count) for use by compilers, assemblers, or other module generators. The CRC is calculated starting at the source address over "byte count" bytes. It is not necessary to cover an entire module in one call, since the CRC may be "accumulated" over several calls. The CRC accumulator must be initialized to \$FFFFFF before the first F\$CRC call.

The last three bytes in the module MUST be \$FFFFFF. This is the initial module CRC and is normally replaced by the calculated value.

DBIT

Deallocate in a bit map

F\$DBIT ____

ASSEMBLER CALL: OS9 FSDBIT

MACHINE CODE: 103F 14

(X) = Base address of an allocation bit map.(D) = Bit number of first bit to clear. INPUT:

(Y) = Bit count (number of bits to clear).

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system mode service request is used to clear bits in the allocation bit map pointed to by X.

Bit numbers range from C. N-1, where N is the number of bits in the allocation bit map.

EXIT

Terminate the calling process.

F\$EXIT

ASSEMBLER CALL: OS9 FEXIT

MACHINE CODE: 103F 06

INPUT: (B) = Status code to be returned to the parent process.

OUTPUT: Process is terminated.

This call kills the calling process. Its data memory area is deallocated, and its primary module is UNLINKed. All open paths are automatically closed.

The death of the process can be detected by the parent executing a WAIT call, which returns to the parent the status byte passed by the child in its EXIT call. The status byte can be an OS-9 error code the terminating process wishes to pass back to its parent process (the shell assumes this), or can be used to pass a user-defined status value. Processes to be called directly by the shell should only return an OS-9 error code or zero if no error occurred.

FORK

Create a new process.

F\$FORK

ASSEMBLER CALL: OS9 F\$FORK

MACHINE CODE: 103F 03

INPUT: (X) = Address of module name or file name.

(Y) = Parameter area size.

(U) = Beginning address of the parameter area.

(A) = Language / Type code.

(B) = Optional data area size (pages).

OUTPUT: (X) = Updated path the name string.

(A) = New process ID number.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system call creates a new process which becomes a "child" of the caller, and sets up the new process' memory and MPU registers.

The system parses the name string of the new process' "primary module" - the program that will initially be executed. Then the system module directory is searched to see if the program is already in memory. If so, the module is linked to and executed. If not, the name string is used as the pathlist of the file which is to be loaded into memory. Then the first module in this file is linked to and executed (several modules may have been loaded from a single file).

The primary module's module header is used to determine the process' initial data area size. OS-9 then attempts to allocate a contiguous RAM area equal to the required data storage size, (includes the parameter passing area, which is copied from the parent process' data area). The new process' registers are set up as shown in the diagram on the next page. The execution offset given in the module header is used to set the PC to the module's entry point.

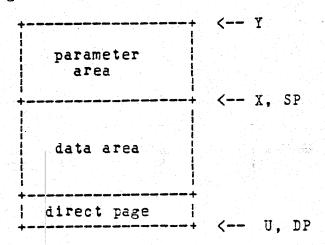
(continued)

FORK (continued)

When the shell processes a command line it passes a string in the parameter area which is a copy of the parameter part (if any) of the command line. It also inserts an end-of-line character at the end of the parameter string to simplify string-oriented processing. The X register will point to the beginning of the parameter string. If the command line included the optional memory size specification (#n or #nK), the shell will pass that size as the requested memory size when executing the FORK.

If any of the above operations are unsucessful, the FORK is aborted and the caller is returned an error.

The diagram below shows how FORK sets up the data memory area and registers for a newly-created process.



D = parameter area size

PC = module entry point abs. address

 $CC = F=\emptyset$, $I=\emptyset$, others undefined

Y (top of memory pointer) and U (bottom of memory pointer) will always have a values at 256-byte page boundaries. If the parent does not specify a parameter area, Y, X, and SP will be the same, and D will equal zero. The minimum overall data area size is one page (256 bytes). Shell will always pass at least an end of line character in the parameter area.

INTERCEPT

Set up a signal intercept trap.

F\$ICPT ____

ASSEMBLER CALL: OS9 FSICPT

MACHINE CODE: 103F 09

(X) = Address of the intercept routine.
 (U) = Address of the intercept routine local storage.

None. OUTPUT:

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system call tells OS-9 to set a signal intercept trap, where X contains the adddress of the signal handler routine, and U contains the base address of the routine's storage area. After a signal trap has been set, whenever the process receives a signal, intercept routine will be executed. A signal will abort any process which has not used the F\$ICPT service request to set a signal trap, and its termination status (B register) will be the signal code. Many interactive programs will set up and intercept routine to handle keyboard abort (control Q), and keyboard interrupt (control C).

intercept routine is entered asynchronously because a signal may be sent at any time (it is like an interrupt) and is passed the following:

U = Address of intercept routine local storage.

B = Signal code.

NOTE: The value of DP may not be the same as it was when the FSICPT call was made.

Whenever a signal is received, OS-9 will pass the signal code and the base address of its data area (which was defined by a FSICPT service request) to the signal intercept routine. The base address of the data area is selected by the user and is typically a pointer to the process data area.

The intercept routine is activated when a signal is received, then it takes some action based upon the value of the signal code such as setting a flag in the process' data area. After the signal has been processed, the handler routine should terminate with an RTI instruction.

GET ID

Get process ID / user ID

F\$ID

ASSEMBLER CALL: OS9 F\$ID

MACHINE CODE: 103F 0C

INPUT: Nothing.

OUTPUT: (A) = Process ID.

(Y) = User ID.

ERROR OUTPUT: (CC) = C Bit set.

(B) = Appropriate error code.

Returns the caller's process ID number, which is a byte value in the range of 1 to 255, and the user ID which is a integer in the range 0 to 65535. The process ID is assigned by OS-9 and is unique to the process. The user ID is defined in the system password file, and is used by the file security system and a few other functions. Several processes can have the same user ID.

LINK:

Link to memory module.

F\$LINK

ASSEMBLER CALL: OS9 F\$LINK

MACHINE CODE: 103F 00

INPUT: (X) = Address of the module name string.

(A) = Module type / language byte.

OUTPUT: (X) = Advanced past the module name.

(Y) = Module entry point absolute address.

(U) = Module header absolute address.

(A) = Module type / language.

(B) = Module attributes / revision level.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropritate error code.

This system call causes OS-9 to search the module directory for a module having a name, language and type as given in the parameters. If found, the address of the module's header is returned in U, and the absolute address of the module's execution entry point is returned in Y (as a convenience: this and other information can be obtained from the module header). The module's link count is incremented whenever a LINK references its name, thus keeping track of how many processes are using the module. If the module requested has an attribute byte indicating it is not sharable (meaning it is not reentrant) only one process may link to it at a time.

Possible errors:

(A) Module not found.

(B) Module busy (not sharable and in use).

(C) Incorrect or defective module header.

LCAD

Load module(s) from a file.

F\$LOAD

ASSEMBLER CALL: OS9 F\$LOAD

MACHINE CODE: 103F 01

INPUT: (X) = Address of pathlist (file name)

(A) = Language / type (Ø = any language / type)

OUTPUT: (X) = Advanced past pathlist

(Y) = Primary module entry point address

(U) = Address of module header

(A) = Language / type

(B) = Attributes / revision level

ERROR OUTPUT: (CC) = C Bit set

(B) = Appropriate error code

Opens a file specified by the pathlist, reads one or more memory modules from the file into memory, then closes the file. All modules loaded are added to the system module directory, and the first module read is LINKed. The parameters returned are the same as the LINK call and apply only to the first module loaded.

In order to be loaded, the file must have the "execute" permission and contain a module or modules that have a proper module header. The file will be loaded from the working execution directory unless the pathlist specifies otherwise.

Possible errors: module directory full; memory full; plus errors that occur on OPEN, READ, CLOSE and LINK system calls.

MIM

Resize data memory area.

F\$MEM

ASSEMBLER CALL: OS9 F\$MEM

MACHINE COLE: 103F 07

INPUT: (D) = Desired new memory area size in bytes.

OUTPUT: (Y) = Address of new memory area upper bound.

(D) = Actual new memory area size in bytes.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Used to expand or contract the process' data memory area. The new size requested is rounded up to the next 256-byte page boundary. Additional memory is allocated contiguously upward (towards higher addresses), or deallocated downward from the old highest address. If $D=\emptyset$, then the current upper bound and size will be returned.

This request can never return all of a process' memory, or the page in which its SP register points to.

In Level One systems, the request may return an error upon an expansion request even though adequate free memory exists. This is because the data area is always made contiguous, and memory requests by other processes may fragment free memory into smaller, scattered blocks that are not adjacent to the caller's present data area. Level Two systems do not have this restriction because of the availability of hardware for memory relocation, and because each process has its own "address space".

PRERR

Print error message.

F\$PERR

ASSEMBLER CALL: OS9 F\$PERR

MACHINE CODE: 103F 0F

INPUT: (A) = Output path number.

(B) = Frror code.

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This is the system's error reporting utility. It writes an error message to the output path specified. Most OS-9 systems will display:

ERROR #<decimal number>

by default. The error reporting routine is vectored and can be replaced with a more elaborate reporting module. To replace this routine use the F\$SSVC service request.

PARSINAME

Parse a path name.

F\$PNAM

ASSEMBLER CALL: OS9 F\$PNAM

MACHINE CODE: 103F 10

INPUT: (X) = Address of the pathlist.

OUTPUT: (X) = Updated past the optional "/"

(Y) = Address of the last character of the name + 1.

(B) = Length of the name.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

(X) = Updated past space characters.

Parses the input text string for a legal OS-9 name. The name is terminated by any character that is not a legal component character. This system call is useful for processing pathlist arguments passed to new processes. Also if X was at the end of a pathlist, a bad name error will be returned and X will be moved past any space characters so that the next pathlist in a command line may be parsed.

Note that this system call processes only one name, so several calls may be needed to process a pathlist that has more than one same.

F\$PNAM CALL:

THE FSPNAM CALL:

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OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Service Request Descriptions - User Mode

SEMAP

Search bit map for a free area

FSSBIT ======

ASSEMBLER CALL: OS9 FSSBIT

MACHINE COLE: 103F 12

INPUT: (X) = Beginning address of a bit map.
(D) = Beginning bit number.
(Y) = Bit count (free bit block size).

(U) = End of bit map address.

OUTPUT: (D) = Beginning bit number.

(Y) = Bit count.

This system mode service request searches the specified allocation bit map starting at the "beginning bit number" for a free block (cleared bits) of the required length.

If no block of the specified size exists, it returns with the carry set, beginning bit number and size of the largest block.

SEND

Send a signal to another process.

F\$SEND

ASSEMBLER CALL: OS9 F\$SEND

MACHINE COIE: 103F 08

INPUT: (A) = Reciever's process ID number.

(B) = Signal code.

OUTPUT: None.

FRROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system call sends a "signal" to the process specified. The signal code is a single byte value of 1 - 255.

If the signal's destination process is sleeping or waiting, it will be activated so that it may process the signal. The signal processing routine (intercept) will be executed if a signal trap was set up (see F\$ICPT), otherwise the signal will about the destination process, and the signal code becomes the exit status (see WAIT). An exception is the WAKEUP signal, which activates a sleeping process but does not cause the signal intercept routine to be executed.

Some of the signal codes have meanings defined by convention:

Ø = System Abort (cannot be intercepted)

1 = Wake Up Process

2 = Keyboard Abort

3 = Keyboard Interrupt

4-255 = user defined

If an attempt is made to send a signal to a process that has an unprocessed, previous signal pending, the current send request will be cancelled and an error will be returned. An attempt can be made to resend the signal later. It is good practice to issue a sleep call for a few ticks before a retry to avoid wasting MPU time.

For related information see the F\$ICPT, F\$WAIT, and F\$SLEP service request descriptions.

SLEEP

Put calling process to sleep.

F\$SLEP

ASSEMBLER CALL: OS9 F\$SLEP

MACHINE CODE: 103F 0A

INPUT: (X) = Sleep time in ticks $(\emptyset = indefinitely)$

OUTPUT: (X) = Decremented by the number of ticks that the

process was asleep.

ERROR OUTPUT: (CC) = C bit set

(B) = Appropriate error code.

This call deactivates the calling process for a specified time, or indefinitely if $X=\emptyset$. The process will be activated before the full time interval if a signal is received, therefore sleeping indefinitely is a good way to wait for a signal without wasting CPU time.

The duration of a "tick" is system dependent but is most commonly 100 milliseconds.

Due to the fact that it is not known when the F\$SLEP request was made durring the current tick, F\$SLEP can not be used for precise timing. A sleep of one tick is effectively a give up remaining time slice request; the process is immediately inserted into the active process queue and will resume execution when it reaches the front of the queue. A sleep of two or more ticks causes the process to be inserted into the active process queue after N-1 ticks occur and will resume execution when it reaches the front of the queue.

SETPR

Set process priority.

F\$SPRI

ASSEMBLER CALL: OS9 FSSPRI

MACHINE CODE: 103F 0D

INPUT: (A) = Process ID number.

(B) = Priority:

Ø = lowest
255 = highest

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Changes the process's priority to the new value given. \$FF is the highest possible priority, \$00 is the lowest. A process can change another process' priority only if it has the same user ID.

SSVC

Install function request

F\$SSVC

ASSEMBLER CALL: OS9 F\$SSVC

ASSEMBLER CODE: 103F 32

INPUT: (Y) = Address of service request initialization table.

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system mode service request is used to add a new function request to OS-9's user and privileged system service request tables, or to replace an old one. The Y register passes the address of a table which contains the function codes and offsets to the corresponding service request handler routines. This table has the following format:

OFFSET

\$00	! Function Code !	< First entry
\$01	! Offset From Byte 3 !	
\$02	! To Function Handler !	
\$03	! Function Code !	< Second entry
\$04	! Offset From Byte 6 !	
\$05	! To Function Handler !	
	MORE ENTRIES	< Third entry etc.
	\$80 !	< End of table mark

NOTE: If the sign bit of the function code is set, only the system table will be updated. Otherwise both the system and user tables will be updated. Privileged system service requests may be called only while executing a system routine.

(continued)

SSVC (continued)

The service request handler routine should process the service request and return from subroutine with an RTS instruction. They may alter all MPU registers (except for SP). The U register will pass the address of the register stack to the service request handler as shown in the following diagram:

			CFFSFT	OS9DEFS NMEMONIC
U>	. cc :		\$Ø	RSCC
	I A !		\$1 \$1	R\$D R\$A
	! B !		\$2	R\$B
	! DP !		\$3	R\$DP
	! X	1	\$4	R\$X
	Y	1	\$€	R\$Y
	! U	!	\$8	R\$U
	! PC	!	\$ A	R\$PC
				

Function request codes are broken into the two categories as shown below:

\$00 - \$28 User mode service request codes.

\$29 - \$34 Privileged system mode service request codes. When installing these service request, the sign bit should be set if it is to be placed into the system table only.

NOTE: These categories are defined by convention and not enforced by OS9.

Codes \$25..\$28, and \$70..\$7F will not be used by MICROWARE and are free for user definition.

SETSWI

Set SWI vector.

F\$SSWI

ASSEMBIER CALL: OS9 F\$SSWI

MACHINE CODE: 103F 0E

INPUT: (A) = SWI type code.

(X) = Address of user SWI service routine.

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Sets up the interrupt vectors for SWI, SWI2 and SWI3 instructions. Each process has its own local vectors. Each SETWSI call sets up one type of vector according to the code number passed in A.

1 = SWI

2 = SWI2

3 = SWI3

When a process is created, all three vectors are initialized with the address of the OS-9 service call processor.

WARNING: Microware-supplied software uses SWI2 to call OS-9. If you reset this vector these programs will not work. If you change all three vectors, you will not be able to call OS-9 at all.

SETIME

Set system date and time.

FSSTIM

ASSEMBLER CALL: OS9 F\$STIM

MACHINE CODE: 103F 16

INPUT: (X) = Address of time packet (see below)

OUTPUT: Time/date is set.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This service request is used to set the current system date/time. The date and time are passed in a time packet as follows:

OFFSET	VALUE
Ø !	year
1 1	month
2 !	day
3 !	hours
4 !	minutes
5 !	seconds

TIME

Get system date and time.

F\$TIME

ASSEMBLER CALL: 059 F\$TIME

MACHINE CODE: 103F 15

INPUT: (X) = Address of place to store the time packet.

OUTPUT: Time packet (see below).

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This returns the current system date and time in the form of a six byte packet (in binary). The packet is copied to the address passed in X. The packet looks like:

OFFSET	VALUE
Ø !	year
1 !	month
2 !	day
3!	hours
4 !	minutes
5 1	seconds

UNLINK

Unlink a module.

F\$UNLK

ASSEMBLER CALL: OS9 F\$UNLK

MACHINE CODE: 103F 02

INPUT: (U) = Address of the module header.

OUTPUT: Nothing.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Tells OS-9 that the module is no longer needed by the calling process. The module's link count is decremented, and the module is destroyed and its memory deallocated when the link count equals zero. The module will not be destroyed if in use by any other process(es) because its link count will be non-zero. In Level Two systems, the module is usually switched out of the process' address space.

Device driver modules in use or certain system modules cannot be unlinked. ROMed modules can be unlinked but cannot be deleted from the module directory.

WAIT

Wait for child to die.

F\$WAIT

ASSEMBLER CALL: OS9 F\$WAIT

MACHINE CODE: 103F 04

INPUT: Nothing.

OUTPUT: (A) = Deceased child's process ID.
(B) = Child's exit status code.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

The calling process is deactivated until a child process terminates by executing an EXIT system call, or by receiving a signal. The child's ID number and exit status is returned to the parent. If the child died due to a signal, the exit status byte (B register) is the signal code.

If the caller has several children, the caller is activated when the first one dies, so one WAIT system call is required to detect termination of each child.

If a child died before the WAIT call, the caller is reactivated almost immediately. WAIT will return an error if the caller has no children.

See the EXIT description for more related information.

A64 Allocate a 64 byte memory block

F\$A64

ASSEMBLER CALL: 0S9 F\$A64

MACHINE CODE: 103F 30

INPUT: (X) = Base address of page table (zero if the page table

has not yet been allocated).

OUTPUT: (A) = Block number.

(X) = Base address of page table.

(Y) = Address of block.

ERROR OUTPUT: (CC) = C bit set.

(P) = Appropriate error code.

This system mode service request is used to dynamically allocate 64 byte blocks of memory by splitting whole pages (256 byte) into four sections. The first 64 bytes of the base page are used as a "page table", which contains the MSB of all pages in the memory structure. Passing a value of zero in the X register will cause the F\$A64 service request to allocate a new base page and the first 64 byte memory block. Whenever a new page is needed, an F\$SRQM service request will automatically be executed. The first byte of each block contains the block number; routines using this service request should not alter it. Below is a diagram to show how 7 blocks might be allocated:

	ANY 256 BYTE MEMORY PAGE	ANY 256 BYTE MEMORY PAGE
BASE PAGE>	PAGE TABLE ! ! (64 bytes) !	!X ! ! BLOCK 4 ! ! (64 bytes) !
	!X ! ! BLOCK 1 ! ! (64 bytes) !	!X ! ! BLOCK 5 ! ! (64 bytes) !
	!X ! ! BLOCK 2 ! ! (64 byte) !	!X ! BLOCK 6 ! ! (64 byte) !
	!X ! BLOCK 3 ! ! (64 byte) !	IX
	4	+

Block numbers range from 1..N

APRC

Insert process in active process queue

F\$APRC

ASSEMBLER CALL: OS9 F\$APRC

MACHINE CODE: 103F 2C

INPUT: (X) = Address of process descriptor.

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system mode service request inserts a process into the active process queue so that it may be scheduled for execution.

All processes already in the active process queue are aged, and the age of the specified process is set to its priority. If the process is in system state, it is inserted after any other processes also in system state, but before any process in user state. If the process is in user state, it is inserted according to its age.

FIND-64

Find a 64 byte memory block

FsF64 ++++

ASSEMBLER CALL: 0S9 F\$F64

MACHINE CODE:

103F 2F

INPUT: (X) = Address of base page.

(A) = Block number.

OUTPUT: (Y) = Address of block.

ERROR OUTPUT:

(CC) = C bit set.

(B) = Appropriate error code.

This system mode service request will return the address of a 64 byte memory block as described in the F\$A64 service request. OS-9 used this service request to find process descriptions and path descriptors when given their number.

Block numbers range from 1..N

IODEL

Delete I/O device from system

F\$IODL

ASSEMBLER CALL: OS9 F\$IODL

MACHINE CODE:

103F 33

INPUT: (X) = Address of an I/O module. (see description)

OUTPUT: None.

ERROR OUTPUT:

(CC) = C bit set.

(B) = Appropriate error code.

This system mode service request is used to determine whether or not an I/O module is being used. The X register passes the address of a device descriptor module, device driver module, or file manager module. The address is used to search the device table, and if found the use count is checked to see if it is zero. If it is not zero, an error condition is returned.

This service request is used primarily by IOMAN and may be of limited or no use for other applications.

IOQUEUE

Enter I/O queue

F\$100U

ASSEMBLER CALL: OS9 F\$100U

MACHINE CODE:

103F 2B

INPUT: (A) = Process Number.

CUTPUT: None.

ERROR OUTPUT: (CC)

(CC) = C bit set.

(B) = Appropriate error code.

This system mode service request links the calling process into the I/O queue of the specified process and performs an un-timed sleep. It is assumed that routines associated with the specified process will send a wakeup signal to the calling process.

SETIRC

Add or remove device from IRO table.

F\$ IR C +++++

ASSEMBLER CALL: OS9 F\$IRO

MACHINE CODE: 103F 2A

INPUT: (X) = Zero to remove device from table, or the address of a packet as defined below to add a device to the IRO polling table:

[x] = flip byte
[X+1] = mask byte
[X+2] = priority

- (U) = Address of service routine's static storage area.
- (Y) = Device IRQ service routine address.
- (D) = Address of the device status register.

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This service request is used to add a device to or remove a device from the IRQ polling table. To remove a device from the table the input should be $(X)=\emptyset$, (U)=Addr of service routine's static storage. This service request is primarily used by device driver routines. See the text of this manual for a complete discussion of the interrupt polling system.

PACKET DEFINITIONS:

Flip Byte

This byte selects whether the bits in the device status register are active when set or active when cleared. A set bit(s) identifies the active bit(s).

Mask Byte

This byte selects one or more bits within the device status register that are interrupt request flag(s). A set bit identifies an active bit(s).

Priority The device priority number:

Z = lowest

255 = highest

NXTPRCS Start next process

F\$NPRC +++++

ASSEMBLER CALL: OS9 F\$NPRC

MACHINE CODE:

103F 2D

INPUT: None.

OUTPUT: Control does not return to caller.

This system mode service request takes the next process out of the Active Process Queue and initiaites its execution. If there is no process in the queue, OS-9 waits for an interrupt, and then checks the active process queue again.

Deallocate a 64 byte memory block

F\$R64

ASSEMBLER CALL: 0S9 F\$R64

MACHINE CODE: 103F 31

INPUT: (X) = Address of the base page.

(A) = Block number.

OUTPUT: None.

R64

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system mode service request deallocates a 64 byte block of memory as described in the F\$A64 service request.

SROMEM

System memory request

F\$SRQM +++++

ASSEMBLER CALL: OS9 F\$SRQM

MACHINE CODE: 103F 28

INPUT: (D) = Byte count.

OUTPUT: (U) = Beginning address of memory area.

ERROR OUTPUT: (CC) =-C bit set.

(E) = Appropriate error code.

This system mode service request allocates a block of memory from the top of available RAM of the specified size. The size requested is rounded to the next 256 byte page boundary.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Service Request Descriptions - System Mode

SRTMEM

Return System Memory

FSSRTM +++++

ASSEMBLER CALL: OS9 F\$SRTM

MACHINE CODE: 103F 29

(U) = Beginning address of memory to return.(D) = Number of bytes to return. INPUT:

OUTPUT: None.

ERROR OUTPUT:

(CC) = C bit set.

(B) = Appropriate error code.

This system mode service request is used to deallocate a block of contiguous 256 byte pages. The U register must point to an even page boundary.

VMOD

Verify module

F\$ VMCD +++++

ASSEMBLER CALL: OS9 F\$VMOD

MACHINE CODE:

103F 2E

INPUT: (X) = Address of module to verify.

OUTPUT: (U) = Address of module directory entry.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system mode service request checks the moule header parity and CRC bytes of an OS-9 module. If these values are valid, then the module directory is searched for a module with the same name. If a module with the same name exists, the one with the highest revision level is retained in the module directory. Ties are broken in favor of the established module.

ATTACH

Attach a new device to the system.

ISATCH

ASSEMBLER CALL: CS9 ISATCH

MACHINE CODE: 103F 80

INPUT: (X) = Address of device name string.

(A) = Access mode.

OUTPUT: (U) = Address of device table entry.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This service request is used to attach a new device to the system, or verify that it is already attached. The device's name string is used to search the system module directory to see if a device descriptor module with the same name is in memory (this is the name the device will be known by). The descriptor module will contain the name of the device's file manager, device driver and other related information. If it is found and the device is not already attached, OS-9 will link to its file manager and device driver, and then place their address' in a new device table entry. Any permanent storage needed by the device driver is allocated, and the driver's initialization routine is called (which usually initializes the hardware).

If the device has already been attached, it will not be reinitialized.

An ATTACH system call is not required to perform routine I/O. It does NOT reserve the device in question — it just prepares it for subsequent use by any process. Most devices are automatically installed, so it is used mostly when devices are dynamically installed or to verify the existance of a device.

The access mode parameter specifies which subsequent read and/or write operations will be permitted as follows:

Ø = Use device capabilities.

1 = Read only.

2 = Write only.

3 = Both read and write.

CHDIR

Change working directory.

I \$ CDIR

ASSEMBLER CALL: OS9 ISCDIR

MACHINE CODE: 103F 86

INPUT: (X) = Address of the pathlist.

(A) = Access mode.

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Changes a process' working directory to another directory file specified by the pathlist. The file must be a directory, and have read permission (public read if not owned by the calling process). New files may be added to the directory only if it has similar write permit attributes.

ACCESS MODES:

1 = Read

2 = Write

3 = Update (read or write)

4 = Execute

If the access mode is read, write, or update the current data directory is changed. If the access mode is execute, the current execution directory is changed.

CLOSE

Close a path to a file/device.

I \$ CLCS ____

ASSEMBLER CALL: OS9 ISCLOS

MACHINE CODE:

103F 8F

INPUT: (A) = Path number.

OUTPUT: None.

ERROR OUTPUT:

(CC) = C bit set.

(E) = Appropriate error code.

Terminates the I/O path specified by the path number. I/O can no longer be performed to the file/device, unless another OPEN or CREATF call is used. Devices that are non-sharable become available to other requesting processes. All OS-S internally managed buffers and descriptors are deallocated.

Note: Because the OS9 F\$EXIT service request automatically closes all open paths (except the standard I/O paths), it may not be necessary to close them individually with the OS9 ISCLOS service request.

Standard I/O paths are not typically closed except when it is desired to change the files/devices they correspond to.

CREATE Create a path to a new file.

I S CREA

ASSEMBLER CALL: OS9 I\$CREA

MACHINE CODE: 103F 83

INPUT: (X) = Address of the pathlist.

(A) = Access mode.
(B) = File attributes.

OUTPUT: (X) = Updated past the pathlist (trailing blanks skipped)

(A) = Path number.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Used to create a new file on a multifile mass storage device. The pathlist is parsed, and the new file name is entered in the specified (or default working) directory. The file is given the attributes passed in the B register, which has individual bits defined as follows:

bit \emptyset = read permit

bit 1 = write permit

bit 2 = execute permit

bit 3 = public read permit

bit 4 = public write permit bit 5 = public execute permit

bit 6 = sharable file

The access mode parameter passed in register A must be either "WRITE" or "UPDATE". This only affects the file until it is closed; it can be reopened later in any access mode allowed by the file attributes (see OPEN). Files open for "WRITE" may allow faster data transfer than "UPDATE", which sometimes needs to pre-read sectors. These access codes are defined as given below:

2 = Write only.

3 = Update (read and write).

NOTE: If the execute bit (bit 2) is set, the file will be created in the working execution directory instead of the working data directory.

(continued)

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Service Request Descriptions - I/O Requests

CREATE (continued)

The path number returned by OS-9 is used to indentify the file in subsequent I/O service requests until the file is closed.

The file's owner is the user who created it. Other users may access the file only if the appropriate permission bit(s) are set in the file attributes byte.

No data storage is initially allocated; this is done automatically by WRITE and PUTSTAT calls.

An error will occur if the file name already exists in the directory. CREATE calls that specify non-multiple file devices (such as printers, terminals, etc.) work correctly: the CREATE behaves the same as OPEN. Create cannot be used to make directory files (see MAKDIR).

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Service Request Descriptions - I/O Requests

DELETE

Delete a file.

I\$DLET

ASSEMBLER CALL: OS9 ISDLET

MACHINE CODE:

103F 87

INPUT: (X) = Address of pathlist.

OUTPUT: (X) = Updated past pathlist (trailing spaces skipped).

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This service request deletes the file specified by the pathlist. The file must have write permission attributes (public write if not the owner), and reside on a multifile mass storage device. Attempts to delete devices will result in an error.

DETACH

Remove a device from the system.

ISDTCH ____=

ASSEMBLER CALL: OS9 ISDTCH

MACHINE CODE: 103F 81

INPUT: (U) = Address of the device table entry.

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Removes a device from the system device table if not in use by any other process. The device driver's termination routine is called, then any permanent storage assigned to the driver is deallocated. The device driver and file manager modules associated with the device are unlinked (and may be destroyed if not in use by another process.

The ISDTCH service request must be used to un-attach devices that were attached with the ISATCH service request. Both of these are used mainly by IOMAN and are of limited (cor no use) to the user. SCFMAN also uses ATTACH/DETACH to setup its second (echo) device.

DUP

Duplicate a path.

I \$ DUP

ASSEMBLER CALL: OS9 I\$DUP

MACHINE CODE: 103F 82

INPUT: (A) = Path number of path to duplicate.

OUTPUT: (B) = New path number.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Given the number of an existing path, returns another synonymous path number for the same file or device. SHELL uses this service request when it redirects I/O. Service requests using either the old or new path numbers operate on the same file or device.

NOTE: This only increments the "use count" of a path descriptor and returns the synonymous path number. The path descriptor is not copied.

GETSTAT

Get file/device status.

I\$GSTT

ASSEMBLER CALL: OS9 I\$GSTT

MACHINE CODE: 103F 8D

INPUT: (A) = Path number. (B) = Status code.

(Other registers depend upon status code)

OUTPUT: (depends upon status code)

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system call is a "wild card" call used to handle individual device parameters that:

a) are not uniform on all devices

b) are highly hardware dependent

c) need to be user-changable

The exact operation of this call depends on the device driver and file manager associated with the path. A typical use is to determine a terminal's parameters for backspace character, delete character, echo on/off, null padding, paging, etc. It is commonly used in conjunction with the SETSTAT service request which is used to set the device operating parameters. Below are the presently defined function codes for GETSTAT:

NMEMONIC	CODE	FUNCTION
SS.OPT	Ø	Read the 32 byte option section of the path descriptor.
SS.RDY	1	Test for data ready on SCFMAN-type device.
SS.SIZ	2	Return current file size (on REFMAN-type devices).
SS.POS	5	Get current file position.
SS.IOF	6	Test for end of file.

(continued)

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Service Request Descriptions - I/O Requests

CODE 7-127

Reserved for future use.

CODE 128-255

These getstat codes and their parameter passing conventions are user definable (see the sections of this manual on writing device drivers). The function code and register stack are passed to the device driver.

Parameter Passing Conventions

The parameter passing conventions for each of these function codes are given below:

SS.OPT (code 0): Read option section of the path descriptor.

INPUT:

(A) = Path number

(B) = Function code 0

(X) = Address of place to put a 32 byte status packet.

OUTPUT: Status packet.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This getstat function reads the option section of the path descriptor and copies it into the 32 byte area pointed to by the X register. It is typically used to determine the current settings for echo, auto line feed, etc. For a complete description or the status packet, please see the section of this manual on path descriptors.

GETSTAT (continued)

SS.RDY (code 1): Test for data available on SCFMAN supported devices.

(A) = Path number. INPUT:

(B) = Function code 1

OUTPUT:

! Not Ready ! Error ! Ready (CC) ! C bit clear ! C bit set ! C bit set ! \$F6 (E\$NRDY) ! ERROR Code (B)

SS.SIZ (code 2): Get current file size (RBFMAN supported devices only)

(A) = Path number. INPUT:

(B) = Function code 2

OUTPUT: (X) = M.S. 16 bits of current file size.

(U) = L.S. 16 bits of current file size.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

SS.POS (code 5): Get current file position (RBFMAN supported devices only).

(A) = Path number INPUT:

(B) = Function code 5

OUTPUT: (X) = M.S. 16 bits of current file position. (U) = L.S. 16 bits of current file position.

(CC) = C bit set. ERROR OUTPUT:

(B) = Appropriate error code.

OS-9 LEVEL CNE SYSTEM PROGRAMMER'S MANUAL Service Request Descriptions - I/O Requests

GETSTAT (continued)

SS.EOF (code 6): Test for end of file.

INPUT: (A) = Path number.

(B) = Function code 6

(00) I Chit Clon I Chit set I Chit set I

(CC) ! C bit Clear ! C bit set ! C bit set !

(B) ! Zero ! \$D3 (E\$EOF) ! Error Code !

MAKDIR

Make a new directory.

I\$MDIR

ASSEMBLER CALL: OS9 I\$MDIR

MACHINE CODE: 103F 85

INPUT: (X) = Address of pathlist.
(B) = Directory attributes.

OUTPUT: (X) = Updated past pathlist (trailing spaces skipped).

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

MAKDIR is the only way a new directory file can be created. It will create and initialize a new directory as specified by the pathlist. The new directory file contains no entries, except for an entry for itself (".") and its parent directory (".")

The caller is made the owner of the directory. MAKDIR does not return a path number because directory files are not opened by this request (use OPEN to do so). The new directory will automatically have its "directory" bit set in the access permission attributes. The remaining attributes are specified by the byte passed in the B register, which has individual bits defined as follows:

bit \emptyset = read permit

bit 1 = write permit
bit 2 = execute permit

bit 3 = public read permit

bit 4 = public write permit

bit 5 = public execute permit
bit 6 = sharable directory

bit 7 = (don't care)

OPEN Open a path to a file or device.

I \$ OPEN

ASSEMBLER CALL: OS9 ISOPEN

MACHINE CODE: 103F 84

INPUT: (X) = Address of pathlist.

(A) = Access mode (D S PE PW PR E W R)

OUTPUT: (X) = Updated past pathlist (trailing spaces skipped).

(A) = Path number.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Opens a path to an existing file or device as specified by the pathlist. A path number is returned which is used in subsequent service requests to identify the file.

The access mode parameter specifies which subsequent read and/or write operations are permitted as follows:

1 = read mode

2 = write mode

3 = update mode (both read and write)

Update mode can be slightly slower because sector pre-reads may be required for random access of bytes. The access mode must conform to the access permision attributes associated with the file or device (see CREATE). Only the owner may access a file unless the appropriate "public permit" bits are set.

Files can be opened by several processes (users) simultaneously. Devices have an attribute that specifies whether or not they are sharable on an individual basis.

NOTES: If the execution bit is set in the access mode, OS-9 will begin searching for the file in the working execution directory (unless the pathlist begins with a slash).

The sharable bit (bit 6) in the access mode can not lock other users out of a file in OS-9 Level I. It is present only for upward compatability with OS-9 Level II.

Directory files may be read or written if the D bit (bit ?) is set in the acces mode.

READ

Read data from a file or device.

I\$READ

ASSEMBLER CALL: OS9 I\$READ

MACHINE CODE: 103F 89

INPUT: (X) = Address to store data.

(Y) = Number of bytes to read.

(A) = Path number.

OUTPUT: (Y) = Number of bytes actually read.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

Reads a specified number of bytes from the path number given. The path must previously have been opened in READ or UPDATE mode. The data is returned exactly as read from the file/device without additional processing or editing such as backspace, line delete, end-of-file, etc.

AFTER all data in a file has been read, the next I\$READ service request will return and end of file error.

NOTES: The keyboard abort, keyboard interrupt, and end-of-file characters may be filtered out of the input data on SCFMAN-type devices unless the corresponding entries in the path descriptor have been set to zero. It may be desirable to modify the device descriptor so that these values in the path descriptor are initialized to zero when the path is opened.

The number of bytes requested will be read urless:

- A. An end-of-file occurs
- B. An end-of-record occurs (SCFMAN only)
- C. An error condition occurs.

READIN

Read a text line with editing.

I\$RDLN

ASSEMBLER CALL: OS9 I\$RDLN

MACHINE CODE: 103F 8B

INPUT: (X) = Address to store data.

(Y) = Maximum number of bytes to read.

(A) = Path number.

OUTPUT: (Y) = Actual number of bytes read.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system call is the same as "READ" except it reads data from the input file or device until a carriage return character is encountered or until the maximum byte count specified is reached.

Line editing will occur on SCFMAN-type devices. Line editing refers to backspace, line delete, echo, automatic line feed, etc.

SCFMAN requires that the last byte entered be an end-of-record character (normally carriage return). If more data is entered that the maximum specified, it will not be accepted and a PD.OVF character (normally bell) will be echoed.

After all data in a file has been read, the next I\$RDLN service request will return an end of file error.

NOTE: For more information on line editing, see the section of this manual on "SCFMAN line editing features".

SEEK Reposition the logical file pointer.

I \$ SEEK

ASSEMBLER CALL: OS9 I\$SEEK

MACHINE CODE: 103F 88

INPUT: (A) = Path number.

(X) = M.S. 16 bits of desired file position. (U) = L.S. 16 bits of desired file position.

OUTPUT: None.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system call repositions the "file pointer"; the 32-bit address of the the next byte in the file to be read from or written to.

A seek may be performed to any value even if the file is not large enough. Subsequent WRITES will automatically expand the file to the required size (if possible), but READs will return an end-of-file condition. Note that a SEEK to address zero is the same as a "rewind" operation.

Seeks to non-random access devices are usually ignored and return without error.

SETSTAT

Set file/device status.

I\$SSTT

ASSEMBLER CALL:

OS9 ISSSTT

MACHINE CODE:

103F 8E

INPUT: (A

(A) = Path number.

(B) = Function code.

(Other registers depend upon the function code).

OUTPUT: (Depends upon the function code).

ERROR OUTPUT:

(CC) = C bit set.

(B) = Appropriate error code.

This system call is a "wild card" call used to handle individual device parameters that:

- a) are not uniform on all devices
- b) are highly hardware dependant
- c) need to be user-chagable

The exact operation of this call depends on the device driver and file manager associated with the path. A typical use is to set a terminal's parameters for backspace character, delete character, echo on/off, null padding, paging etc. It is commonly used in conjuction with the GETSTAT service request which is used to read the device's operating parameters etc. Below are the presently defined function codes:

NMEMONIC CODE	FUNCTION
SS.OPT \$0	Write the 32 byte option section of the path descriptor to set the device
SS.SIZ \$2	Set the file size (for RBFMAN type devices only).
SS.RST \$3	Restore head to track zero.
SS.WRT \$4	Write track.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Service Request Descriptions - I/O Requests

SETSTAT (continued)

CODES 5-127

Reserved for future use.

CODES

128-255

These SETSTAT codes and their parameter passing conventions are user definable (see the sections of this manual on writing device drivers). The function code and register stack are passed to the device driver.

Parameter Passing Conventions

The parameter passing conventions for each of these function codes is given below:

SS.OPT (code Ø): Write option section of path descriptor.

INPUT:

(A) = Path number

(B) = Function code \emptyset

(X) = Address of a 32 byte status packet

OUTPUT: None.

ERROR OUTPUT:

(CC) = C bit set

(B) = Appropriate error code.

This setstat function writes the option section of the path descriptor from the 32 byte status packet pointed to by the X register. It is typically used to set the device operating parameters, such as echo, auto line feed, etc. For a complete description of what is in the status packet, please see the section of this manual on path descriptors.

```
SETSTAT (continued)
```

SS.SIZ (code 2): Set file size (RBFMAN-type devices) (A) = Path number INPUT: (B) = Function code 2(X) = M.S. 16 bits of desired file size. (U) = L.S. 16 bits of desired file size. OUTPUT: None. ERROR OUTPUT: (CC) = C bit set (B) = Appropriate error code. This setstat function is used to set the file size (REFMAN supported devices only). SS.RST (code 3): Restore head to track zero. (A) = Path number INPUT: (B) = Function code 3 OUTPUT: None (CC) = C bit set ERROR OUTPUT: (B) = Appropriate error code SS.WTK (code 4): Write track. (A) = Path numberINPUT: (B) = Function code 4(X) = Address of track buffer. (U) = Track number (L.S. 3 bits) (Y) = Side/densityBit BØ = SIDE (Ø = side zero, 1 = side one)Bit B1 = DENSITY (\emptyset = single, 1 = double) OUTPUT: None (CC) = C bit set ERROR OUTPUT: (B) = Appropriate error code

WRITE

Write data to a file or device.

I\$WRIT

argum appears from all the state of

ASSEMBLER CALL:

OS9 I\$WRIT

MACHINE CODE:

103F 8A

INPUT:

(X) = Address of data to write.

(Y) = Number of bytes to write.

(A) = Path number.

OUTPUT: (Y) = Number of bytes actually written.

ERROR OUTPUT:

(CC) = C bit set.

(B) = Appropriate error code.

WRITE outputs one or more bytes to a file or device associated with the path number specified. The path must have been OPENed or CREATEED in the WRITE or UPDATE access modes.

Data is written to the file or device without processing or editing. If data is written past the present end-of-file, the file is automatically expanded.

WRITELN

Write a line of text with editing.

ISWRLN ======

ASSEMBLER CALL: OS9 I SWRLN

103F 8C MACHINE CODE:

INPUT: (X) = Address of data to write.
(Y) = Maximum number of bytes to write.

(A) = Path number.

OUTPUT: (Y) = Actual number of bytes written.

ERROR OUTPUT: (CC) = C bit set.

(B) = Appropriate error code.

This system call is similar to WRITE except it writes data until a carriage return character is encountered. Line editing is also activated for character-oriented devices such as terminals, printers, etc. The line editing refers to auto line feed, null padding at end-of-line, etc.

For more information about line editing, see the section of this manual on "SCFMAN Line Editing Features".

WRITING RBF-TYPE DEVICE DRIVERS

An RBF type device driver module contains a package of subroutines that perform sector oriented I/O to or from a specific hardware controller. These modules are usually reentrant so that one copy of the module can simultaneously run several different devices that use identical I/O controllers. IOMAN will allocate a static storage area for each device (which may control several drives). The size of the storage area is given in the device driver module header. Some of this storage area will be used by IOMAN and RBFMAN, the device driver is free to use the remainder in any way that it desires. This static storage is laid out as follows:

Static Storage Definitions

OFFSET		ORG	Ø	
Ø 1 3 4 5	V.PAGE V.PORT V.LPRC V.BUSY V.WAKE V.USER	RMB RMB RMB RMB RMB	2 1 1 1	PORT EXTENDED ADDRESS DEVICE BASE ADDRESS LAST ACTIVE PROCESS ID ACTIVE PROCESS ID (Ø = NOT BUSY) PROCESS ID TO REAWAKEN END OF OS9 DEFINITIONS
6 7	V.NDRV DRVBEG TABLES FREE	RMB EQU RMB EQU	D.	NUMBER OF DRIVES BEGINNING OF DRIVE TABLES RVMEM*N RESERVE N DRIVE TABLES FREE FOR DRIVER TO USE

NOTE: V.PAGE through V.USER are predefined in the OS9DEFS file. V.NDRV, DRVBEG, DRVMEM are predefined in the RBFDEFS file.

V.PAGE, V.PORT These three bytes are defined by IOMAN to be the 24 bit device address.

V.LPRC This location contains the process-ID of the last process to use the device. Not used by RBF-type device drivers.

V.BUSY This location contains the process-ID of the process currently using the device. Defined by RBFMAN.

V.WAKE This location contains the process-ID of any process that is waiting for the device to complete I/O (\emptyset = NO PROCESS WAITING). Defined by device driver.

V. NDRV

This location contains the number of drives that the controller will be working with. Defined by the device driver as the maximum number of drives that the controller can work with. RBFMAN will assume that there is a drive table for each drive. Also see the driver INIT routine in this section.

TABLES

This area contains one table for each drive that the controller will handle (RBFMAN will assume that there are as many tables as indicated by V.NDRV). Some time after the driver INIT routine has been called. RBFMAN will issue a request for the driver to read the identification sector cm a drive. At this will initialize the (logical sector zero) from a drive. driver the time. corresponding drive table by copying the first part of the identification sector (up to DD.SIZ) "Identification Sector into it. Also see the section of this manual. The format of each drive table is as given below:

OFFSET		ORG Ø	
\$98468BDE0115	DD.DIR	RMB 3 RMB 1 RMB 2 RMB 2 RMB 3 RMB 2 RMB 1 RMB 1 RMB 2 RMB 2 RMB 2 RMB 2	TOTAL NUMBER OF SECTORS TRACK SIZE (IN SECTORS) # BYTES IN ALLOCATION BIT MAP NUMBER OF SECTORS / BIT ADDRESS OF ROOT DIRECTORY OWNER'S USER NUMBER DISK ATTRIBUTES DISK ID MEDIA FORMAT SECTORS/TRACK RESERVED FOR FUTURE USE
\$15 \$17 \$18	V.TRAK V.BMB DRVMEM	RMB 2 RMB 1 EQU .	CURRENT TRACK NUMBER BIT-MAP USE FLAG SIZE OF EACH DRIVE TABLE

DD.TOT This location contains the total number of sectors contained on the disk.

DD.TKS This location contains the track size (in sectors).

DD.MAP This location contains the number of bytes in the disk allocation bit map.

- DD.BIT This location contains the number of sectors that each bit represents in the disk allocation bit map.
- DD.DIR This location contains the logical sector number of the disk root directory.
- DD.OWN This location contains the disk owner's user number.
- DD.ATT This location contains the disk access permission attributes as defined below:

BIT 7 = D (DIRECTORY IF SET)
BIT 6 = S (SHARABLE IF SET)
BIT 5 = PX (PUBLIC EXECUTE IF SET)
BIT 4 = PW (PUBLIC WRITE IF SET)
BIT 3 = PR (PUBLIC READ IF SET)
BIT 2 = X (EXECUTE IF SET)
BIT 1 = W (WRITE IF SET)
BIT 0 = R (READ IF SET)

DD.DSK This location contains a pseudo random number which is used to identify a disk so that OS-9 may detect when a disk is removed from the drive and another inserted in its place.

DD.FMT DISK FORMAT:

BIT BØ - SIDE Ø = SINGLE SIDED 1 = DOUBLE SIDED

BIT B1 - DENSITY

Ø = SINGLE DENSITY

1 = DOUBLE DENSITY

BIT B2 - TRACK DENSITY

Ø = SINGLE (48 TPI)

1 = DOUBLE (96 TPI)

DD.SPT Number of sectors per track (track zero may use a different value, specified by IT.TØS in the device descriptor).

DD.RES RESERVED FOR FUTURE USE

V.TRAK This location contains the current track which the head is on and is updated by the driver.

Appendix A: Writing RBF-type Device Drivers

V.BMB This location is used by REFMAN to indicate whether or not the disk allocation bit map is currently in use (\emptyset = not in use). The disk driver routines must not alter this location.

Other Important Parameters

Other parameters which may be important to device drivers can found in the path descriptor. For a complete description of the values which are contained in the path descriptor, please see the section of this manual on "RBFMAN Definitions of The Path Descriptor". Also see the section on device descriptors (especially the initialization table).

RBFMAN Device Driver Subroutines

As with all device drivers, RBFMAN-type device drivers use a standard executable memory module format with a module type of "device driver" (CODE SE). The execution offset address in the module header points to a branch table that has six three byte entries. Each entry is typically a LBRA to the corresponding subroutine. The branch table is defined as follows:

ENTRY	LBRA	INIT	INITIALIZE DRIVE
	LBRA	READ	READ SECTOR
	LBRA	WRITE	WRITE SECTOR
	LBRA	GETSTA	GET STATUS
	LBRA		SET STATUS
	LBRA	TERM	TERMINATE DEVICE

Each subroutine should exit with the condition code register C bit cleared if no error occured. Otherwise the C bit should be set and an appropriate error code returned in the B register. Below is a description of each subroutine, its input parameters, and its output parameters.

NAME: INIT

INPUT: (U) = ADDRESS OF DEVICE STATIC STORAGE

(Y) = ADDRESS OF THE DEVICE DESCRIPTOR MODULE

OUTPUT: NONE

ERROR OUTPUT: (CC) = C BIT SET

(B) = ERROR CODE

FUNCTION:

INITIALIZE DEVICE AND ITS STATIC STORAGE AREA

1. If disk writes are verified, use the F\$SRQM service request to allocate a 256 byte buffer area where a sector may be read back and verified after a write.

- 2. Initialize the device permanent storage. For floppy disk controller typically this consists of initializing V.NDRV to the number of drives that the controller will work with, initializing DD.TOT in the drive table to a non-zero value so that sector zero may be read or written to, and initializing V.TRAK to \$FF so that the first seek will find track zero.
- 3. Place the IRQ service routine on the IRQ polling list by using the OSS F\$IRQ service request.
- 4. Initialize the device control registers (enable interrupts if necessary).

NOTE: Prior to being called, the device permanent storage will be cleared (set to zero) except for V.PAGE and V.PORT which will contain the 24 bit device address. The driver should initialize each drive table appropriately for the type of disk the driver "expects" on the corresponding drive.

NAME: READ

INPUT: (U) = ADDRESS OF THE DEVICE STATIC STORAGE

(Y) = ADDRESS OF THE PATH DESCRIPTOR

(B) = MSB OF DISK LOGICAL SECTOR NUMBER

(X) = LSB's OF DISK LOGICAL SECTOR NUMBER

CUTPUT: SECTOR IS RETURNED IN THE SECTOR BUFFER

ERROR OUTPUT: (CC) = C BIT SET

(B) = APPROPRIATE ERROR CODE

FUNCTION: READ A 256 BYTE SECTOR

Read a sector from the disk and place it in the sector buffer (256 byte). Below are the things that the disk driver must do:

- 1. Get the sector buffer address from PD.BUF in the path descriptor.
- 2. Get the drive number from PD.DRV in the path descriptor.
- 3. Compute the physical disk address from the logical sector number.
- 4. Initiate I/O.
- 5. Move V.BUSY to V.WAKE, then go to sleep and wait for the I/O to complete (the IRQ service routine is responsible for sending a wake up signal). After awakening, test V.WAKE to see if it is clear, if not, go back to sleep.

If the disk controller can not be interrupt driven it will be necessary to perform programmed I/O.

NOTE1: Whenever logical sector zero is read, the first part of this sector must be copied into the proper drive table (get the drive number from PD.DRV in the path descriptor). The number of bytes to copy is DD.SIZ.

NOTE2: The drive number (PD.DRV) should be used to compute the offset to the corresponding drive table as follows:

LDA PD.DRV,Y Get drive number
LDB #DRVMEM Get size of a drive table
MUL
LEAX DRVBEG,U Get address of first table
LEAX D.X Compute address of table N

NAME: WRITE

INPUT: (U) = ADDRESS OF THE DEVICE STATIC STORAGE AREA

(Y) = ADDRESS OF THE PATH DESCRIPTOR

(B) = MSB OF THE DISK LOGICAL SECTOR NUMBER
(X) = LSB's OF THE DISK LOGICAL SECTOR NUMBER

OUTPUT: THE SECTOR BUFFER IS WRITTEN OUT TO DISK

ERROR OUTPUT: (CC) = C BIT SET

(B) = APPROPRIATE ERROR CODE

FUNCTION: WRITE A SECTOR

Write the sector buffer (256 byte) out to the disk. Below are the things that a disk driver must do:

- 1. Get the sector buffer address from PD.BUF in the path descriptor.
- 2. Get the drive number from PD.DRV in the path descriptor.
- 3. Compute the physical disk address from the logical sector number.
- 4. Initiate I/C.
- 5. Move V.BUSY to V.WAKE, then go to sleep and wait for the I/O to complete (the IRQ service routine is responsible for sending the wakeup signal). After awakening, test V.WAKE to see if it is clear, if it is not, then go back to sleep.

If the disk controller can not be interrupt driven, it will be necessary to perform programmed I/O.

- 6. If PD.VFY in the path descriptor is equal to zero, read the sector back in and verify that it was written correctly. This usually does not involve a compare of the data.
- NOTF1: If disk writes are to be verified, the INIT routine must request the buffer where the sector may be placed when it is read back in. Do not copy sector zero into the drive table when it is read back to be verified.
- NOTE3: Use the drive number (PD.DRV) to compute the offset to the corresponding drive table as shown for the READ routine.

NAME: GETSTA PUTSTA

INPUT: (U) = ADDRESS OF THE DEVICE STATIC STORAGE AREA

(Y) = ADDRESS OF THE PATH DESCRIPTOR

(A) = STATUS CODE

OUTPUT: (DEPENDS UPON THE FUNCTION CODE)

ERROR OUTPUT: (CC) = C BIT SET

(B) = APPROPRIATE ERROR CODE

FUNCTION: GET / SET DEVICE STATUS

These routines are wild card calls used to get (set) the device's operating parameters as specified for the OS9 I&GSTT and I&SSTT service requests. The codes passed to the device driver are given below:

GETSTAT: Any I\$GSTT function code not defined by Microware

SETSTAT: SS.RST (code 3) = Restore head to track zero.

SS.WRT (code 4) = Write track.

Any ISSTT function code not defined by Microware.

It may be necessary to examine or change the register stack which contains the values of MPU registers at the time of the I\$GSTT or I\$SSTT service request. The address of the register stack may be found in PD.RGS, which is located in the path descriptor. The following offsets may be used to access any particular value in the register stack:

OFFSET	NMEMON	IC	MPU REGISTER		
\$ 1123468A	R\$CC R\$D R\$A R\$B R\$DP R\$X R\$Y R\$PC	RMB 1 EQU . RMB 1 RMB 1 RMB 2 RMB 2 RMB 2 RMB 2 RMB 2	CONDITION CODE REGISTER D REGISTER A REGISTER B REGISTER DP REGISTER X REGISTER Y REGISTER U REGISTER PROGRAM COUNTER		

NAME: TERM

INPUT: (U) = ADDRESS OF DEVICE STATIC STORAGE AREA

OUTPUT: NONE

ERROR OUTPUT: (CC) = C BIT SFT

(B) = APPROPRIATE ERROR CODE

FUNCTION: TERMINATE DEVICE

This routine is called when a device will no longer be needed in the system (when the link count goes to zero). The primary things that it does are:

- Wait until any pending I/O has completed.
- 2. Disable the device interrupts.
- 3. Remove the device from the IRQ polling list.
- 4. If the INIT routine reserved a 256 byte buffer for verifying disk writes, return the memory with the F\$MEM service request.

NAME: THE IRQ SERVICE ROUTINE

FUNCTION: SERVICE DEVICE INTERRUPTS

Although this routine is not included in the device drivers branch table and not called directly from RBFMAN, it is an important routine in device drivers. The main things that it does are:

- 1. Service device interrupts.
- 2. When the I/O is complete, the IRO service routine should send a wake up signal to the process whose process ID is in V.WAKE

Also clear V.WAKE as a flag to the mainline program that the IRO has indeed occurred.

NOTE: When the IRQ service routine finishes servicing an interrupt it must clear the carry and exit with an RTS instruction.

WRITING SCF-TYPE DEVICE DRIVERS

An SCFMAN-type device driver module contains a package of subroutines that perform raw I/O transfers to or from a specific hardware controller. These modules are usually reentrant so that one copy of the module can simultaneously run several different devices that use identical I/O controllers. For each "incarnation" of the driver, IOMAN will allocate a static storage area for that device. The size of the storage area is given in the device driver module header. Some of this storage area will be used by IOMAN and SCFMAN, the device driver is free to use the remainder in any way it desires (typically as variables and buffers). This static storage is laid out as given below:

STATIC STORAGE DEFINITIONS

OFFSET		ORG Ø	
\$4	V.LPRC V.BUSY		PORT EXTENDED ADDRESS DEVICE BASE ADDRESS LAST ACTIVE PROCESS ID ACTIVE PROCESS ID (@ = NOT BUSY) PROCESS ID TO REAWAKEN END OF OS9 DEFINITIONS
89 B C D	V.INTR V.QUIT	RMB 1 RMB 2 RMB 1 RMB 1 RMB 1	DEVICE TYPE OR PARITY LINES LEFT TILL END OF PAGE PAUSE REQUEST (Ø = NO PAUSE) ATTACHED DEVICE STATIC STORAGE INTERRUPT CHARACTER QUIT CHARACTER PAUSE CHARACTER ERROR ACCUMULATOR END OF SCFMAN DEFINITIONS
	FREE	ECU .	FREE FOR DEVICE DRIVER TO USE

V.PAGE, V.PORT These three bytes are defined by IOMAN to be the 24 bit device address.

V.LPRC This location contains the process-ID of the last process to use the device. The IRQ service routine is responsible for sending this process the proper signal in case a "QUIT" character or an "INTERRUPT" character is recieved. Defined by SCFMAN.

V.ERR

This location is used to accumulate I/O errors. Typically it is used by the IRO service routine to record errors so that they may be reported later when SCFMAN calls one of the device driver routines.

SCFMAN DEVICE DRIVER SUBROUTINES

As with all device drivers. SCFMAN device drivers use a standard executable memory module format with a module type of "device driver" (CODE \$E). The execution offset address in the module header points to a branch table that has six three byte entries. Fach entry is typically a LBRA to the corresponding subroutine. The branch table is as follows:

ENTRY	LERA	INIT	INITIALIZE DEVICE
	LBRA	READ	READ CHARACTER
	LPRA	WRITE	WRITE CHARACTER
	LBRA	GETSTA	GET DEVICE STATUS
	LBRA	SETSTA	SET DEVICE STATUS
	LBRA	TERM	TERMINATE DEVICE

Each subroutine should exit with the condition code register C bit cleared if no error occured. Otherwise the C bit should be set and an appropriate error code returned in the B register. Eelow is a description of each subroutine, its input parameters and its output parameters.

NAME: INIT

INPUT: (U) = ADDRESS OF DEVICE STATIC STORAGE

(Y) = ADDRESS OF DEVICE DESCRIPTOR MCDULE

OUTPUT: NONE

ERROR OUTPUT: (CC) = C BIT SET

(B) = ERROR CODE

FUNCTION:

INITIALIZE DEVICE AND ITS STATIC STORAGE

1. Initialize the device static storage.

2. Place the IRQ service routine on the IRQ polling list by using the OS9 F\$IRQ service request.

3. Initialize the device control registers (enable interrupts if necessary).

NOTE: Prior to being called, the device static storage will be cleared (set to zero) except for V.PAGE and V.PORT which will contain the 24 bit device address. There is no need to initialize the portion of static storage used by IOMAN and SCFMAN.

NAME: READ

INPUT: (U) = ADDRESS OF DEVICE STATIC STORAGE

(Y) = ADDRESS OF PATH DESCRIPTOR

OUTPUT: (A) = CHARACTER READ

TRROR OUTPUT: (CC) = C BIT SET

(B) = ERROR CODE

FUNCTION: GET NEXT CHARACTER

This routine should get the next character from the input buffer. If there is no data ready, this routine should copy its process ID from V.BUSY into V.WAKE and then use the F\$SLEP service request to put itself to sleep.

Later when data is recieved, the IRQ service routine will leave the data in a buffer, then check V.WAKE to see if any process is waiting for the device to complete I/O. If so, the IRQ service routine should send a wakeup signal to it.

NOTE: Data buffers are NOT automatically allocated. If any are used, they should be defined somewhere in the device's static storage area.

NAME: WRITE

INPUT: (U) = ADDRESS OF DEVICE STATIC STORAGE

(Y) = ADDRESS OF THE PATH DESCRIPTOR

(A) = CHAR TO WRITE

OUTPUT: NONE

ERROR OUTPUT: (CC) = C BIT SET

(B) = ERROR CODE

FUNCTION: OUTPUT A CHARACTER

This routine places a data byte into an output buffer and enables the device output interrupts. If the data buffer is already full, this routine should copy its process ID from V.BUSY into V.WAKE and then put itself to sleep.

Later when the IRQ service routine transmits a character and makes room for more data in the buffer, it will check V.WAKE to see if there is a process waiting for the device to complete I/O. If there is, it will send a wake up signal to that process.

Note: This routine must ensure that the IRQ service routine will start up when data is placed into the buffer. After an interrupt is generated the IRQ service routine will continue to transmit data until the data buffer is empty, and then it will disable the device's "ready to transmit" interrupts.

Note: Data buffers are NOT automatically allocated. If any are used, they should be defined somewhere in the device's static storage.

GETSTA NAME: SETSTA

(U) = ADDRESS CF DEVICE STATIC STORAGE (Y) = ADDRESS OF PATH DESCRIPTOR

(A) = STATUS CCDE

OUTPUT: (DEPENDS UPON FUNCTION CODE)

FUNCTION: GET / SET DEVICE STATUS

This routine is a wild card call used to get (set) the device parameters specified in the ISGSTT and ISSSTT service requests. Currently all of the function codes defined by Microware for SCF-type devices are handled by IOMAN or SCFMAN. Any codes not defined by microware will be passed to the device driver.

It may be necessary to examine or change the register packet which contains the values of the 6809 registers at the time the OS9 service request was issued. The address of the register packet may be found in PD.RGS, which is located in the path descriptor. The following offsets may be used to access any particular value in the register packet:

OFFSET	NMEMONI	C	MPU REGISTI	CR COLOR
\$Ø \$1 \$1	R\$CC R\$D R\$A	RMB 1 EQU . RMB 1	CONDITIONS D REGISTER A REGISTER	CODE REGISTER
\$2 \$3 \$4 \$6	R\$B R\$DP R\$X R\$Y	RME 1 RME 1 RMB 2 RMB 2	B REGISTER DP REGISTER X REGISTER Y REGISTER	
\$8 \$A	R\$U R\$PC	RMB 2 RMB 2	U REGISTER	JNTER

NAME: TERM

INPUT: (U) = PTR TO DEVICE STATIC STORAGE

OUTPUT: NONE

ERROR OUTPUT: (CC) = C bit set

(B) = Appropriate error code

FUNCTION: TERMINATE DEVICE

This routine is called when a device will no longer be needed (when its link count goes to zero). The main things that it does are:

- 1. Wait until the output buffer has been emptied (by the IRQ service routine).
- 2. Disable device interrupts.
- 3. Remove device from the IRQ polling list.

NOTE: Static storage used by device drivers is never returned to the free memory pool. Therefore, it is desirable to NEVER terminate any device that might be used again. Modules contained in the BOOT file will NEVER be terminated.

NAME: IRQ SERVICE ROUTINE

FUNCTION:

SERVICE DEVICE INTERRUPTS

Although this routine is not included in the device drivers branch table and not called directly from SCFMAN, it is an important routine in device drivers. The main things that it does are:

- 1. Service the device interrupts (recieve data from device or send data to it). This routine should put its data into and get its data from buffers which you have defined in the device static storage.
- 2. Wake up any process waiting for I/O to complete by checking to see if there is a process ID in V.WAKE (non-zero) and if so send a wakeup signal to that process.
- 3. If the device is ready to send more data and the output buffer is empty, disable the device's "ready to transmit" interrupts.
- 4. If a pause character is recieved, set V.PAUS in the attached device static storage to a non-zero value. The address of the attached device static storage is in V.DEV2.

When the IRQ service routine finishes servicing an interrupt, it must clear the carry and exit with an RTS instruction.

WRITING A SYSTEM BOOTSTRAP MODULE

The bootstrap module contains one subroutine that loads the bootstrap file and some related information into memory. It uses the standard executable module format with a module type of "system" (code \$C). The execution offset in the module header contains the offset to the entry point of this subroutine. The following section describes the parameters passed to the the BOOT routine and its function. Also see the sections of this manual on "writing RBF-type Device Drivers" and "Logical and Physical Disk Organization".

NAME: BOOT

INPUT: None.

OUTPUT: (D) = SIZE OF THE BOOT FILE (in bytes)

(X) = ADDRESS OF WHERE THE BOOT FILE WAS LOADED IN MEMORY

ERROR OUTPUT: (CC) = C BIT SET

(B) = APPROPRIATE ERROR CODE

FUNCTION: LOAD THE BOOT FILE INTO MEMORY FROM MASS-STORAGE

This routine attempts to load the bootstrap file into memory from a mass-storage device. Typically it will read some form of identification block which will contain the location and size of the bootstrap file. OS-9 is called to allocate a memory area large enough for the boot file, and then it loads the boot file into this memory area. Below is a description of how this is done for RBFMAN-type devices (DISK):

1. Read the identification sector (sector zero) from the disk. BOOT must pick its own buffer area.

OS-9 LEVEL ONE SYSTEM PROGRAMMER'S MANUAL Appendix C: Writing a System Bootstrap Module

The identification sector contains the values for DD.BT (the 24 bit logical sector number of the bootstrap file), and DD.ESZ (the size of the bootstrap file in bytes). For a full description of the identification sector, please see the section on "Physical and Logical Disk Organization".

- 2. After reading the identification sector into the buffer, get the 24 bit logical sector number of the bootstrap file from DD.BT.
- 3. Get the size (in bytes) of the bootstrap file from DD.BSZ. The boot is contained in one logically contiguous block beginning at the logical sector specified in DD.BT and extending for (DD.BSZ/256 + 1) sectors.
- 4. Use the OS9 F\$SROM service request to request the memory area where the boot file will be loaded into.
- 5. Read the boot file into this memory area.
- 6. Return the size of the boot file and its location.

OS-9 LEVEL ONE SYSTEM PROGRAMMERS MANUAL Appendix D: Memory Module Format Diagrams

MODULE OFFSET	EXECUTABLE MEMORY MODULE	FORMAT	
\$00 \$01	! ! + Sync Bytes (\$87CD)+ ! !	1 1 1 1 1 1	
\$02 \$03	!		
\$04 \$05	!	header parity	
\$06	! Type ! Language !		
\$07	! Attributes ! Revision !		! module
\$0 8	! Header Parity Check !		CRC
\$09	! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! !		
\$ØA			
\$ØB \$ØC	! Permanent Storage Size		
\$ØD	! (Add'l optional header ! extensions located here) !		
	Module Body lobject code, constants, etc.		
	CRC Check Value		

			+
\$Ø	! + Sync Bytes (\$87CD)+		
\$1	1		
\$2	! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! !		
\$3	!		1
\$4	! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! ! !	! header	
\$5	!	parity	
\$ 6	! \$F (TYPE) ! \$1 (LANG) !		
\$7	! Atributes ! Revision !		
\$8	! Header Parity Check !		
\$9	!		
\$ A	+ Offset to File Manager+ ! Name String !		module CRC
\$B	!		1
\$ C	+ Offset to Device Driver+ ! Name String !		
\$ D	! Mode Byte		
\$ E			
\$F	+ Device Controller+ ! Absolute Physical Address!		
\$10	+ (24 bit)+ !		
\$11	! Option Table Size		
\$12,\$12+N	! (Option Table) !		
	! (Name Strings etc) !!		
	1		
	! CRC Check Value !		!

MODULF OFFS ET	CONFIGURATION MODULE FORMAT		
\$00	! + Sync Bytes (\$87CD)		
\$01	1		
\$02	! + Module Size (bytes)		
\$03	1		1
\$04	! + Module Name Offset	! header	
\$05	! 	parity!	
\$Ø6	! \$C (TYPE) ! Ø (LANG) !		
\$07	! Attributes ! Revision !	+	! module
\$08	! Header Parity Check !	erie (1905), et j Santa (1905), et j Banda (1905), et j	CRC!
\$09	Forced Limit of Top		
\$ØA	! of Free RAM		
\$ØB			
\$0C	! # IRQ Polling Table Entries		
\$ØD	! # Device Table Entries		
\$ E	! + Offset to Startup		
\$ØF	! Module Name Sring		
\$10	! + Offset to Default Mass		
\$11	! Storage Device Name String		
\$12	! + Offset to Bootstrap		
\$13	! Module Name String		
\$14	! Name Strings		
	4 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 -		1
	! CRC Check Value		
		+	

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OS-9 LFVEL ONE SYSTEM PROGRAMMERS MANUAL Appendix E: OS-9 Floppy Disk Formats

SINGLE DENSITY FLOPPY DISK FORMAT

SIZF	5 "		8"		
DENSITY	SINGLE		SINGLE		
#TRACKS	35		77		
#SECTORS/TRACK	10		16		
BYTES/TRACK (UNFORMATTED)	3125		5208		
FORMAT	#BYTES (DEC)	VALUE (HEX)	#BYTES (DEC)	VALUE (HEX)	
HEADER (ONCE PER TRACK)	3Ø 6	FF 00	30 6	FF ØØ	
	1 12	FC FF	1 12	FC FF	
SECTOR	6	00	6	ØØ	
(REPEATED N TIMES)	1 1 1 1 1 10 6	FE (TRK #) (SIDE #) (SECT #) (BYTCNT) F7 (2 CRC) FF	1 1 1 1 1 1 1 1 1 0 6	FE (TRK #) (SIDE #) (SECT #) (BYTCNT) F7 (2 CRC) FF 00	
	1 256 1 10	FB (DATA) F7 (2 CRC) FF	1 256 1 10	FB (DATA) F7 (2 CRC) FF	
TRAILER (ONCE PER TRACK)	96	II	391	FF	
BYTES/SECTOR (FORMATTED)	256		256		
BYTES/TRACK (FORMATTED)	2560		4096		
BYTES/DISK 89 (FORMATTED)	9,600		315,392		

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OS-9 LEVEL ONE SYSTEM PROGRAMMERS MANUAL Appendix F: OS-9 Floppy Disk Formats

DOUBLE DENSITY FLOPPY DISK FORMAT

				8. 3. 3. 3. 3. 3. 3. 3. 3. 3. 3. 3. 3. 3.	
SIZE	5 "				
DENSITY	DOUBLE			DOUBLE	
#TRACKS	35			77	
#SECTORS/TRACK	16	16		28	
BYTES/TRACK (UNFORMATTED)	6250			10,416	
FORMAT	BYTES (DEC)	VALUE (HEX)	BYTES (DEC)	VALUE (HEX)	
HEADER (ONCE PER TRACK)	80 12	4I 00	80 12	41 00	
	3 1 32	F5 (A1) FC 41	3 1 32	F5 FC 4E	
SECTOR (REPEATED N TIME	12 3 1 1 1 1 22 12 3 1 256 1 22	00 F5 FE (TRK #) (SIDF #) (SECT #) (EYTCNT) F7 (2 CRC) 4E 00 F5 (A1) FB (DATA) F7 (2 CRC)	12 3 1 1 1 1 22 12 3 1 256 1 22	00 F5 FE (TRK #) (SIDE #) (SECT #) (BYTCNT) F7 (2 CRC) 4E 00 F5 (A1) FB (DATA) F7 (2 CRC) 4E	
TRAILER (ONCE PER TRACK	682	41	768	4E	
BYTES/SECTOR (FORMATTED)	256		256		
BYTES/TRACK (FORMATTED)	4096		7168		
BYTES/DISK (FORMATTED)	141,824	548,864			
				· •	

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```
*
                                                    ×
            CLOCK MODULE FOR THE MPT TIMER
 *
                                                    4:
            (C) 1981 Microware Systems Corporation
 *
                                                    *
 *
 ***********************
            Clock Module
        MAM
        TTL
            Definitions
            /d0/defs/systype
        Use
        opt
 ******
 * System Type Definitions *
 ****
             GIMIX
                      CPU type
 CPUTYP
        SET
                      Disk Controller type
             DCB4
DSKTYP
        SET
                      Clock type
         SET
             MPT
  CLKTYP
                      Interrupt Driven Disk Flag
         SET
             YES
 INTRPT
                      Number of Drive Descriptors
         SET
 DRVCNT
             4
                      Drive size
 DRVSIZ
         SET
             8
                      Revision Level
 REV
         SET
             1
 ******
  * Disk Port Address
 *********
         SET
             0
  DPORT
  DPORT
         SET
             $E600
  *********
  * Clock Port Address
  ***
  CPORT
         SET
             Ø
  CPORT
         SET
             $E050
  *********
  * I/O Port Addresses
  *****
                      ACIA Master Terminal
              $E004
  A.TERM
         SET
                      ACIA Secondary Terminal
         SET
              $E020
  A.T1
              $E040
                      PIA Printer (B-side)
  A.P
         SET
  PIASID
              a.side
         set
                      ACIA Printer
  A.P1
         SET
              $ EØ3Ø
         opt
              C
         opt
              -C
```

OS-9 LEVEL ONE SYSTEM PROGRAMER'S MANUAL Appendix F: Sample Assembly Language Programs - CLOCK

```
*******
     MODULE HEADER
************
             SYSTM+OBJCT
        SET
Type
        SET
             REENT+1
Revs
             ClkEnd, ClkNam, Type, Revs, ClkEnt, CPORT
ClkMod
        Mod
        FCS
             /Clock/
ClkNam
                       Edition number
        FCB
             2
                       Stack has Clock Port address
             MSSTAK
CLKPRT
        FQU
********
* CLOCK DATA DEFINITIONS *
********
        FCB
            FSTIME
TIMSVC
        FDB
             TIME-*-2
        FCB
             $80
*********
  DAYS IN MONTHS TABLE
*******
                       UNINITIALIZED MONTH
             Ø
MONTHS
        FCB
                       JANUARY
             31
        FCB
                       FEBRUARY
        FCB
             28
        FCB
             31
                       MARCH
                       APRIL
        FCB
             3Ø
             31
                       MAY
        FCB
                       JUNE
        FCB
             30
                       JULY
        FCB
             31
                       AUGUST
        FCB
             31
        FCB
             30
                       SEPTEMBER
                       OCTOBER
        FCB
             31
```

30

31

FCB

FCB

* CLOCK INTERRUPT SERVICE ROUTINE * ****

[D.ISVC] GO TO INTERRUPT SERVICE JMP NOTCLK CLKPRT, PCR GET CLOCK ADDRESS LDX CLKSRV GET CONTROL REGISTER LDA 1,X IS IT CLOCK? BITA #\$80 BRANCH IF NOT BEO NOTCLK CLEAR CLOCK INTERRUPT LDA Q.X SET DIRECT PAGE CLRA TICK TFR A, DP

NOVEMBER

DECEMBER

```
COUNT TICK
               D.TIC
        DEC
                           BRANCH IF NOT END OF SECOND
               TICK50
         BNE
                           GET MINUTE & SECOND
               D.MIN
         LDD
                           COUNT SECOND
         INCB
                           END OF MINUTE?
         CMPB
               #60
                           BRANCH IF NOT
         PCS
               TICK35
                           COUNT MINUTE
         INCA
                           END OF HOUR?
         CMPA
               #6Ø
                           BRANCH IF NOT
               TICK3Ø
         BCS
                           GET DAY & HOUR
               D.DAY
         LDD
                           COUNT HOUR
         INCB
                           END OF DAY?
         CMPB
               #24
                           BRANCH IF NOT
               TICK25
         PCS
                           COUNT DAY
         INCA
               MONTHS . PCR GET DAYS/MONTH TABLE
         T. EAX
                           GET MONTH
         LDB
               D.MNTH
                           IS IT FEBRUARY?
         CMPB
               #2
                           BRANCH IF NOT
         BNE
                TICK10
                           GET YEAR
         LDB
                D.YEAR
                           BRANCH IF EVEN HUNDRED
         BEQ
                TICK10
                           IS IT LEAP YEAR?
                #3
         ANDB
                           BRANCE IF NOT
                TICK10
         BNE
                           ADD FEB 29
         DECA
                           GET MONTH
         LDB
                HTMM. C
TICK10
                           END OF MONTH?
         CMPA
                P.X
                           BRANCH IF NOT
         BLS
                TICK20
                           GET YEAR & MONTH
                D.YEAR
         LDD
                            COUNT MONTH
         INCB
                            END OF YEAR?
         CMPB >
                #13
                            BRANCH IF NOT
                TICK15
         BCS
                            COUNT YEAR
         INCA
                            NEW MONTH
         LDB
                #1
                            UPDATE YEAR & MONTH
         STD
                D.YEAR
TICK15
                            NEW DAY
         LDA
                #1
                            NEW HOUR
         CLRB
TICK20
                            UPDATE DAY & HOUR
                D.DAY
         SID
TICK25
                            NEW MINUTE
          CLRA
                            NEW SECOND
          CLRB
TICK3Ø
                            UPDATE MINUTE & SECOND
                D.MIN
          SID
TICK35
                            GET TICKS/SECOND
                D.TSEC
          LDA
          STA
                D.TIC
                        GO TO SYSTEM CLOCK ROUTINE
                [CLOCK]
          JMP
TICK50
```

```
********
* CLOCK INITIALIZATION ENTRY
*************
         PSHS
                          save Direct Page
              \mathbf{DP}
ClkEnt
                          clear DP
         CLRA
         TFR
               A, DP
                          save interrupt masks
              CC
         PSHS
                          SET TICKS / SECOND
         LDA
               #10
         STA
               D.TSEC
         STA
               D.TIC
                          SET TICKS / TIME-SLICE
         LDA
               #1
               D.TSLC
         STA
         STA
               D.SLIC
               #IROM+FIROM SET INTRPT MASKS
         ORCC
               CLKSRY, PCR GET SERVICE ROUTINE
         LEAX
                          SET INTERRUPT VECTOR
         STX
               D.IRO
         LDX
               CLKPRT. PCR get clock address
         CLRA
         CLRB
                          CLEAR PIA REGS.
         STD
               Ø,X
                          INITIALIZE CLOCK BOARD
         LDD
               #sFF3D
         SID
               Ø,X
         LDD
               #$8005
         STA
               Ø,I
         STB
               Ø,X
                          CLEAR ANY INTERRUPTS
               Ø,X
         LDA
                          retrieve masks
               CC
         PULS
               TIMSVC, PCR
         LEAY
                          SET TIME SEVICE ROUTINE
               F$SSVC
         059
         PULS
               DP.PC
**********
     SUBROUTINE TIME
   (RETURN TIME OF DAY) *
********
         ECU
TIME
                          Get Specified location
         LDX
               R$X,U
                          Get Year & Month
         LDD
               D.YEAR
         SID
               \emptyset.X
               D.DAY
                          Get Day & Hour
         LDD
         STD
               2,X
                          Get Minute & Second
               D.MIN
         LDD
         STD
               4.X
                          Clear Carry
         CLRB
         RTS
         EMod
         EQU
ClkEnd
         END
```

*

*

*

```
************************
            TERM -- Device Descriptor Module
*
×
            (C) 1981 Microware Systems Corporation
*
*****************
             TRMEND, TRMNAM, DEVIC+OBJCT, REENT+1, TRMMGR,
        mod
        fcb
              UPDAT.
                         mode
               SFF
         fcb
               A. TERM
                         port addresss
         fdb
              TRMNAM-#-1 option byte count
         fcb
                         Device Type: SCF
         fcb
              DT.SCF
* DEFAULT PARAMETERS
                         case=UPPER and lower
               0
         fcb
                         backspace=BS.SP.BS
         fcb
               1
                         delete=backspace over line
         fcb
               Ø
         fcb
               1
                         auto echo on
                         auto line feed on
         fcb
               1
         fcb
               Ø
                         null count
                         end of page pause on
         fcb
               1
         fcb
                         lines per page
               24
                         backspace char
               CSBSP
         fcb
                         delete line char
         fcb
               C$DEL
                         end of record char
         fcb
              CSCR
                         end of file char
         fcb
               CSEOF
                          reprint line char
         fcb
               CSRPRT
                          dup last line char
               CSRPET
         fcb
               CSPAUS
                          pause char
         fcb
                          Keyboard Interrupt char
               C$ INTR
         fcb
                          Keyboard Quit char
         fcb
               C$QUIT
                          backspace echo char
         fcb
               C$ ESP
               C$BELL
                          line overflow char
         fcb
         fcb
               $15
                          no parity
                          undefired baud rate
         fcb
               Ø
         fdb
               TRMNAM
                          offset of echo device
                TERM
                          device name
         fcs
TRMNAM
               "SCF
                          file manager
         fcs
TRMMGR
               'ACIA"
                          device driver
         fcs
TRMDRV
                          Module CRC
         emod
```

TRMEND EQU

```
***********
                                                        *
*
       ACIA - Interrupt Driven ACIA Device Driver
                                                        22
        (C) 1981 Microware Systems Corporation
*
***********************
             ACIA
        NAM
        ift1
                      d0/defs/scfdefs
        endc
                       input buffer SIZE (<=256)
             100
INPSIZ
        set
                       output buffer SIZE (<=256)
OUTSIZ
        set
             40
                      parity error bit
PARITY
        set
             %010000000
             200100000
                      overrun error bit
OVERUN
        set
                       framing error bit
             200010000
        set
FRAME
                       not clear to send
             200001000
NOTCTS
        set
             200000100
                       data carrier lost
DCDLST
        set
             PARITY+OVERUN+FRAME+NOTCTS+DCDLST
INPERR
        set
*******
* Static storage offsets *
*******
                       room for SCF variables
             V.SCF
        ORG
                       input buffer NEXT-IN ptr
input buffer NEXT-OUT ptr
        RMB
             1
INXTI
INXTO
      RMB
            1 1
                       output buffer NEXT-IN ptr
ONXTI
        RMB
             1
                       output buffer NEXT-OUT ptr
        RMB
             1
ONXTO
                       input buffer
        RMB
             INPSIZ
INPBUF
                       output buffer
        RMB
             OUTSIZ
OUTBUE
                       TOTAL STATIC STORAGE REQUIREME
ACIMEM
        FOU
*********
    MODULE HEADER
*******
             ACIEND, ACINAM. DRIVR+OBJCT, REENT+1, ACIENT,
        MOD
             UPDAT:
        FCB
        FCS
              ACIA
ACINAM
                       editiion number
        fcb
             2
*********
     BRANCH TABLE
*******
ACIENT
        LBRA
             INIT
        LBRA READ
        LERA WRITE
        LBRA GETSTA
        LBRA PUTSTA
        LERA TRMNAT
```

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```
OS-9 LEVEL ONE SYSTEM PROGRAMER'S MANUAL Appendix F: Sample Assembly Language Programs - ACIA
```

```
no FLIP bits
        FCB
ACMASK
                         IRO POLLING MASK
        FCB
              $80
                         (low) PRIORITY
              5
        FCB
******
* INITIALIZE (TERMINAL) ACIA *
*******
              V.PORT.U I/O port address
        LDX
INIT
                         master reset signal
        LDB
              #$03
        STB
                         reset ACIA
              Q \cdot X
                         get option byte count
              M$OPT.Y
        LDA
              #PD.PAR-PD.OPT acia control value given?
        CMPA
                         .. No; default $15
        BLO
              INIT10
              PD.PAR-PD.OPT+M$DTYP,Y
        LDB
              INIT20
        BNE
                         default acia control
              #$15
INIT10
        LDB
              V.TYPE, U
                         save device type
        STB
INIT20
        LDD
              V.PORT.U
        LEAX
              ACMASK, PCR
              ACIRO, PCR address of INTERRUPT SERVICE R
        LEAY
                         ADD to IRQ POLLING TABLE
         0.59
              FSIRO
                         ERROR - return it
              INIT9
         BCS
         CLRA
         CLRB
                         INITIALIZE buffer ptrs
         STD
               INXTI,U
         STD
              ONXTI,U
               V.PORT.U
         LDX
INIT30
         LDB
              V.TYPE.U
                         Enable ACIA input interrupts
         ORB
               #$8Ø
                         initialize ACIA for input inte
         STB
               C.X
                         return (carry clear)
INIT9
         RTS
*********
        return ONE BYTE of input from the ACIA *
* READ:
*
                                               *
* PASSED: (Y)=PATH DESCRIPTOR
                                               *
          (U)=STATIC STORAGE address
* RETURNS: (A)=input BYTE (carry clear)
           CC=SET, B=ERROR code if error
**************
                         wait for acia data
READØØ
         BSR
               ACSLEP
                         (input buffer) NEXT-OUT ptr
         LDB
               INXTO,U
READ
                         address of input buffer
         LEAX
               INPBUF, U
                         calm interrupts
         ORCC
               #IROM
                         any data AVAILABLE?
         CMPB
               INXTI.U
                          .. No; wait, and retry
               READØØ
         PEQ
         ABX
                          the char
         LDA
               Ø,X
                          ADVANCE NEXT+OUT ptr
         INCB
         CMPB
               #INPSIZ-1
                         end of circular buffer?
               READ10
                          ..No
         BLS
                          reset ptr to start of buffer
         CLRB
                          save updated Buffer ptr
         STB
               INXTO.U
READ10
         CLRB
                        Microware Systems Corporation
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```

F-9

```
OS-9 LEVEL ONE SYSTEM PROGRAMER'S MANUAL Appendix F: Sample Assembly Language Programs - ACIA
```

```
LDB V.ERR,U
                         Transmission error?
                         .. No; return
        BEC
              READ90
                         return error bits in PD
        STB
              PD.ERR, Y
        CLR
              V.ERR,U
        COMB
                         return carry set
                         signal read error
        LDB
             #E$RD
        ANDCC #$FF-IRQM enable IRQ requests
READ90
        RTS
**********
* ACSLEP - Sleep for I/O activity
* This version HOGS CPU if signal pending
                                         *
* PASSED: (cc)=IRQ's MUST be disabled
*
         (U)=Global Storage
                                         *
         V.BUSY.U=current proc id
*
* DESTROYS: possibly PC
********
              D.X
ACSIEP
        PSHS
                        get current process id
              V.BUSY, U
        LDA
                         arrange wake up signal
        STA
              V.WAKE.U
        ANDCC #$FF-IRCM interrupts ok now
        LDX
              #0
        059
              FSSLEP
                        wait for input data
         LDX
              D. PROC
                         signal present?
        LDB
              P$SIGN, X
                         .. No; return
              ACSL9Ø
         bea
         cmpb #S$INTR
                         Deadly signal?
                         .. Yes; return error
              ACSLER
         bls
                         clear carry
ACS L90
         CLRB
                         return
              D.X.PC
         PULS
                          Exit to caller's caller
 ACSLER
         LEAS 6.S
                         return carry set
         COMA
         RTS
***********
* WRITE char THROUGH ACIA
                                              *
                                              쌇
* PASSED: (A)=char to write

* (Y)=PATH DESCRIPTOR
          (U)=STATIC STORAGE address
*
* RETURNS: CC=SET IF BUSY (output buffer FULL)
******************
                         sleep a bit
WRITØØ
         PSR
               ACSLEP
                         output buffer address
              OUTBUF, U
WRITE
         LEAX -
                         (output) NEXT-OUT ptr
         LDB
              U. ITXNC
         ABX
                         PUT char in buffer
         STA
              Ø.X
                         ADVANCE the ptr
         INCB
                         end of circular buffer?
         CMPB
              #OUTSIZ-1
         ELS
               WRIT10
                         reset ptr to start of buffer
         CLRB
                         disable interrupts
         ORCC
               #IROM
WRIT10
                         buffer FULL?
         CMPB
               U, OTXNO
                        Microware Systems Corporation
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```

F-10

```
OS-9 LEVEL ONE SYSTEM PROGRAMER'S MANUAL
Appendix F: Sample Assembly Language Programs - ACIA
```

```
.. Yes; sleep and retry
        BEC
              WRITØØ
                         save updated NEXT-IN ptr
        STE
              U.ITXNO
        ANDCC #$FF-IROM
                         enable IROs
                         PARITY CONTROL
              V.TYPE, U
        LDA
                         ENABLE input/output IRQS
        ORA
              #5 AØ
              [V.PORT.U] ENABLE INTERRUPTS
        STA
                         (return carry clear)
        CLRE
WRIT90
        RTS
***********
* GET/PUT ACIA STATUS
                                    *
* PASSED: (A)=STATUS_CCDE
          (Y)=PATH DESCRIPTOR
         (U)=STATIC STORAGE address *
*
* RETURNS: varies
*********
                         READY STATUS?
GETSTA
         CMPA
              #SS.RDY
                         ..No
         BNE
              GETS10
         LDA
              INXTO, U
                         any data AVAILABLE?
         SUBA
              INXTI.U
                         .. Yes; return carry clear
              WRIT90
         ENE
         COMB
         LDB
              #E$NRDY
         RTS
                         End of file?
              #SS.EOF
GETS10
         CMPA
                          .. Yes; Return carry clear
              WRIT90
         PEQ
PUTSTA
         COMB
                         return carry set
                         UNKNOWN SERVICE CODE
         LDB
               #E$USVC
         RTS
*******
* TERMINATE ACIA processing
* PASSED: (U)=STATIC STORAGE *
* RETURNS: NOTHING
*********
                         wait for I/O activity
         BSR
               ACSLEP
TRMNØØ
         LDX
               D.PROC
TRMNAT
         LDA
               PSID.X
         STA
               V.BUSY, U
         STA
               V.LPRC, U
         LDB
               ONXTI.U
                          disable interrupts
         ORCC
               #IRQM
                          output done?
         CMPB
               U.OTXNO
               TRMNØØ
                          .. No; sleep a bit
         BEO
         LDA
               #$Ø3
               [V.PORT,U] disable ACIA interrupts
         STA
                          enable interrupts
         ANDCC #$FF-IRQM
         LDX
               #0
                          remove acia from polling tbl
               FSIRO
         059
         RTS
                         Microware Systems Corporation
```

F-11

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```
*************************
        Process INTERRUPT (input or output) from ACIA *
                                                     *
* PASSED: (U)=STATIC STORAGE addr
                                                     *
         (X)=Port address
                                                     *
         (A)=polled status
* Returns: NOTHING
*************
              V.PORT,U
                        get port address
ACIRO
        LDX
        ANDA
              #INPERR
                        mask status error bits
        ORA
              V.ERR.U
        STA
              V.ERR.U
                        update cumulative errors
        LDA
                        restore acia status
              0,X
        BITA
              #1
                        input ready?
        BNE
              INACIA
                        ..yes; go get it
**********
* FAIL THROUGH to DO output
*
 OACIA:
          Output to ACIA INTERRUPT ROUTINE
                                          *
                                          *
* PASSED: (A)=ACIA STATUS REGISTER CONTENTS
                                          25
*
         (X)=ACIA port address
         (U)=STATIC STORAGE address
*****************
OACIA
        LEAY
              OUTBUF, U
                        output buffer ptr
        LDB
              ONXTO,U
                        (output) NEXT-OUT ptr
                        output buffer already empty?
        cmpb
              ONXTI.U
                        .. Yes; disable output IRQ, ret
              OACIAZ
        beg:
        CLRA
        LDA
                        next output char
              D.Y
                        ADVANCE NEXT-OUT ptr
        INCB
        CMPB
                        end of circular buffer?
              #OUTSIZ-1
        BLS
              OACIA1
                        ..No
        CLRB
OACIA1
        STB
              U.OTXNO
                        save updated NEXT-OUT ptr
                        WRITE the char
        STA
              1, X
        CMPB.
              ONXTI.U
                        last char in output buffer?
        BNE
              WAKEUP
                        - No
OACIA2
        LBSR
              INIT30
                        disable output rdy IRQ
WAKEUP
              #S SWAKE
                        WAKE UP SIGNAL
        LDB
              V.WAKE, U
                        OWNER WAITING?
        IDA
                        .. No; return
WAKE1@
        PEO
              WAKE90
        059
              FSSEND
WAKESØ
        clr
              V.WAKE.U
        RTS
```

```
******************
* INACIA:
           Process ACIA input INTERRUPT
                                            ×
*
* PASSED: (A)=ACIA STATUS REGISTER data
                                            *
*
          (X)=ACIA port address
                                            *
*
          (U)=STATIC STORAGE address
*
* NOTICE the ABSENCE of ERROR CHECKING HERE *
**************
                          READ input char
INACIA
               1.X
         LDA
               INPBUF.U input buffer
         LEAX
                         (input) NEXT-IN ptr
               INXTI:-U
         LDB
         ABX
                          save char in buffer
         STA
               Ø.X
                          update NEXT-IN ptr
         INCB
                          end of circular buffer?
               #INPSIZ-1
         CMPB
                          ..No
         BLS
               ACIA2
         CLRB
         CMPB
                          input OVERRUN?
ACIA2
               INXTO.U
                          .. No; good
         BNE
               ACIA25
               #OVERUN
                          mark overrun error
         LDB
         ORB
               V.ERR,U
         STB
               V.ERR.U
                          throw away character
         BRA
               ACIA26
                          update NEXT-IN ptr
ACIA25
         STB
               INXTI.U
         ANDA
               #$7F
ACIA26
                          .. pass nulls without ctl check
         PEO
               WAKEUP
                          PAUSE char?
         CMPA
              V.PCHR,U
         BNE
               ACIAS
                          . . No
               V.DEV2,U
                          PAUSE DEVICE STATIC
         LDX
               WAKEUP
                          ..None
         BEO.
                          REQUEST PAUSE
         STA
               V.PAUS,X
         BRA
               WAKEUP
                          INTERRUPT SIGNAL
         LDB
               #SSINTR
ACIA3
                          keyboard INTERRUPT SIGNAL?
         CMPA
               V.INTR.U
                          ..Yes
         PEQ
               ACIA4
         LDB
               #S$ABT
                          ABORT SIGNAL
         CMPA
                          keyboard ABORT SIGNAL?
               V.QUIT,U
                          ..No
         ENE
               WAKEUP
         IDA
               V.LPRC, U
                          last process ID
ACIA4
                          SEND ERROR SIGNAL
               WAKE10
         BRA
                          Module CRC
         emod
ACIEND
         EQU
```

23

1

```
*************
*
        PRINTERR - English Error Printer Module
*
        (C) 1981 Microware Systems Corporation
*
*
****************
            Printerr
       nam
             /d0/defs/os9defs
       use
        ifp1
        endc
**********
* Printerr
                                                 *
   Translate OS-9 error numbers to English messages
* Author: Bob Doggett
* Replaces OS-9 PRTERR service routine.
* Note: once this is done, there is provision
* for returning to OS-S's original error routine.
* Speed could be improved, using fixed-length
                                                  *
* random file. The text file format used by this
* version may be edited, and is shorter than
                                                  22
                                                  *
* a random file would be.
                                                  *
                                                  ×
* CAUTION: this version uses quite a chunk of
                                                  *
* User's stack, and may be unsuitable for some
  processes.
*****************
             PEREND, FERNAM, PRGRM+CBJCT, REENI+1, PRTERR.
        mod
              Printerr
        fcs
PERNAM
                      edition number
             3
        fcb
                      ERRMSG FILE MAX RCD LENGTH
        SET
             80
BUFSIZ
             $ØD
C$ CR
        SET
*********
* Execution-Time Stack temporary storage *
**********
        ORG
             Ø
                      User's Std Error path
        RMB
PRTPTH
             1
                      ERRMSG path number
ERRPTH
        RMB
             1
                      Error number
        RMB
             1
ERRNUM
                      Line buffer ptr
        RMB
BUFPTR
             BUFSIZ
                      Line buffer
        RMB
IOBUFF
        EQU
PERMEM
             "/DØ/"
        FCC
FRRFIL
             "ERRMSG"
        FCC
        FCB
             C$CR
              Error #
        FCC
ERRMSG
        FCB
             -1
                      Microware Systems Corporation
```

F-14

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```
OS-9 LEVEL ONE SYSTEM PROGRAMER'S MANUAL Appendix F: Sample Assembly Language Programs - PRINTERR
```

The state of the s

```
Replacement for SYS call vecto
SVCTBL
         equ
         fcb
               F$PERR
               PERROR-*-2
         fdb
                          end of table
         fcb
               $80
**********************
* Printerr:
                                                   *
    Translate OS-9 errors to English messages
*
                                                   *
    using message strings in ERRMSG file
*
                                                   *
*
                                                   *
* Format of ERRMSG file:
                                                   *
                    Ascii error number (0-255)
*
   Number
            (Ø-3)
                                                   ×
*
                    Any byte <= $2F
   Delim
               1
                   Variable length message string *
   Message (Ø-n)
****
PRTERR
         CLRA
         LEAX
               CPERNAM . PCR
         089
               F$LINK
                          Link to self
                          .. Error; Fail
         BCS
               EXIT
               <SVCTBL,PCR</pre>
         LEAY
                          redirect system prterr routine
               F$SSVC
         059
         CLRB
               F$EXIT
EXIT
         059
               D.PROC
PERROR
         LDX
               P$PATH+2,X Get user's std error path
         LDA
         BEQ
               PERR90
                          .. None; exit
         LEAS
               -PERMEM.S chop out temp storage
         LDB
               R$B.U
         LEAU
               Ø,S
         STA
               PRTPTH.U
         STE
                          save error number
               ERRNUM. U
                          print Error #n
         BSR
               PRTNUM
               #READ.
         LDA
               ERRFIL, PCR ERRMSG file name
         LEAX
         059
               I SOPEN
         STA
                          save path number
               ERRPTH, U
         ECS
                           .. ERROR; exit
               PERR90
                          find error in ERRMSG file
         BSR
               SEARCH
                          .. Not found; exit
         ENE
               PERR80
         BSR
               PRTLIN
                          print Error Message
PERR80
         LDA
               ERRPTH.U
                          close ERRMSG file
         059
               ISCLOS
PERR90
         LEAS
               PERMEM, S
         RTS
SEARCH
         LDA
               ERRPTH. U
         LEAX
               IOBUFF, U
         LDY
               #BUFSIZ
         059
               ISRDLN
                          read one ERRMSG RCD
                           .. FRROR; EXIT
         BCS
               SEAR90
                          get number in I/O bufter
         BSR
               GETNUM
                          Followed by separator?
         CMPA
               # 0
        (C) 1980, 1981 Microware Systems Corporation
```

F-15

```
.. no; skip this record
        BHS
              SEARCH
                         Is this the Error number?
        CMPB ERRNUM, U
                          .. No; REPEAT
              SEARCH
        BNE
        RTS
                         RETURN
SEARSØ
*********
* PRINUM: PRINT 8-BIT ASCII NUMBER IN (,X+) *
***
              ERRMSG, PCR ptr to Error #"
         LEAX
PRINUM
         LEAY
              IOBUFF.U
         LDA
               , X +
               , Y +
         STA
PRT NØ5
         LDA
               , X +
              PRTNØ5
         BPL
         LDB
               ERRNUM.U
         LDA
               # 0-1
                          form hundreds digit
         INCA
PRTN10
         SUBB
               #100
         PCC
               PRTN10
                          Put hundreds digit in buffer
         STA
               , Y+
               #'9+1
         LDA
                          form tens digit
         DECA
PRTN20
         ADDB
               #10
         BCC
               PRT N20
                          Put tens digit in buffer
               , Y+
         STA
         TIR
               B, A
               # 0
                          form units digit
         ADDA
               #C$CR
         LDB
                         Put units digit in buffer
         STD
               , Y +
               IOBUFF.U
         LEAX
PRTLIN
         LDY
               #80
                         Print to STD Error path
PRINT
         LDA
               PRTPTH, U
         059
               ISWRIN
         RTS
         CLRB
GETNUM
               , X+
         LDA
GETN10
               #'0
         SUBA
                          Numeric character?
         CMPA
               #9
                          .. No; done
         BHI
               GET N90
                          save digit
         PSHS
               A
         LDA
               #10
                          multiply partial result by 10
         MUL
                          add in next digit
         ADDB
               ,S+
                          .. Continue until overflow
         BCC
               GETN10
                          retreive separator character
GET NSØ
         LDA
               -1.X
         RTS
                          Module CRC
         emod
PERIND IQU
```

OS-9 LEVEL ONE SYSTEM PROGRAMMERS MANUAL Appendix G: Service Request Summary

SERVICE REQUEST SUMMARY

US ER	MODE FUI	NCTION REQU	ESTS	
CODE		MNEMONIC	FUNCTION	PAGE
103 F F 103 F F F F F F F F F F F F F F F F F F F	Ø1 Ø23 Ø4 Ø5 Ø6 Ø7 Ø89 Ø8 Ø8 Ø8 Ø8 Ø8 Ø8 Ø8 Ø8 Ø8 Ø8 Ø8 Ø8 Ø8	KD KD KD KD KD KD KD KD KD KD	Link to memory module Load module from mass-storage Unlink module	7798365918 214667563 677667563
SYST	EM MODE	PRI VILEGED	FUNCTION REQUESTS	
CODE		MNEMONIC	FUNCTION	PAGE
103F 103F 103F 103F 103F 103F 103F 103F	2A 2B 2C 2D 2E 2F 30 31 32	F\$SRTM F\$SRTM F\$SRTQ F\$SRTC F\$SIRQU F\$SIRQu F\$	System memory request System memory return Enter IRQ polling table Enter I/O queue Enter active process queue Start next process Validate module Find 64 byte memory block Allocate a 64 byte memory block Return a 64 byte memory block Install a function request Eelete I/O module	- 87 - 68 - 64 - 85 - 85 - 85 - 61 - 79 - 66 - 72

^{*}NOTE: F\$SSVC is a user mode function, although its code > \$27

OS-9 LEVEL ONE SYSTEM PROGRAMMERS MANUAL Appendix G: Service Request Summary

INPUT/OUTPUT SERVICE REQUESTS

CODE		MNEMONIC	FUNCTION	PAGE
103F 103F 103F 103F 103F 103F 103F 103F	81 82 83 84 85 86 87 88 88 88 88 88 80 80	I \$ AT CH I \$ DT CH I \$ DUP I \$ CREA I \$ OPEN I \$ ODIR I \$ CDIR I \$ CDIR I \$ DLET I \$ SEEK I \$ READ I \$ WRIT I \$ WRIT I \$ CRIT I	Attach I/O device Detach I/O device Duplicate path Create a new file Open a path to an existing file Make a directory file Change working directory Delete a file Reposition file pointer Read date Write data Get device status	PAGE 90 97 93 102 102 105 106 111 98 107
103F 103F		I\$SSTT I\$CLOS	Set device status	92

ST	ANDARD	1/0	PA	THS	

Ø = Standard Input

1 = Standard Output 2 = Standard Error Output

FILE ACCESS CODES

READ	=	\$01	14.7 * -
WRITE	=	\$02	
UPDATE.		READ	+ WRITE
EXEC	=	\$04	
PREAD	=	\$ 08	
PWRIT		\$10	
PEXEC	=	\$20	
SHARE	=	1	
DIR	=	\$82	

MODULE TYPES

\$1 = Program \$2 = Subroutine Module

\$3 = Multi-Module

\$4 = Data

\$C = System Module

\$D = File Manager \$E = Device Driver

\$F = Device Descriptor

MODULE LANGUAGES

\$0 = Data

\$1 = 6809 Object code

\$2 = BASIC09 I-Code

\$3 = Pascal P-Code

MODULE ATTRIBUTES

\$8 = Reentrant

OS-9 LEVEL CNE SYSTEM PROGRAMMERS MANUAL Appendix H: OS-9 Error Codes

OS-9 ERROR CODES

The error codes are shown in both hexadecimal (first column) and decimal (second column). Error codes other than those listed are generated by programming languages or user programs.

HEX DEC

- \$CS 200 PATH TABLE FULL The file cannot be opened because the system path table is currently full.
- \$09 201 ILLEGAL PATH NUMBER Number too large or for non-existant path.
- \$CA 202 INTERRUPT POLLING TABLE FULL
- \$CB 203 ILLEGAL DEVICE Can't find device descriptor, file manager or device driver.
- \$CC 204 DEVICE TABLE FULL Can't add another device.
- \$CD 205 ILLEGAL MCDULE HEADER Module's sync code, check character or CRC is incorrect.
- \$CE 206 MODULE DIRECTORY FULL Can't add another module
- SCF 207 MEMORY FULL Not enough contiguous RAM available to process request.
- \$DØ 208 ILLEGAL SERVICE REQUEST System call had an illegal code number.
- \$D1 209 MODULE BUSY non-sharable module is in use by another process.
- \$D2 210 BOUNDARY ERROR Memory allocation or deallocation request not on page boundary.
- \$D3 211 END OF FILE End of file encountered on read.
- \$D4 212 NOT YOUR MEMORY attempted to deallocate memory not previously assigned.
- \$D5 213 NON-EXISTING SEGMENT device has damaged file structure.
- \$D6 214 NO PERMISSION you don't have owner's permission to access the file as requested.
- \$D7 215 BAD PATH NAME syntax error in pathlist.
 - (C) 1980, 1981 Microware Systems Corporation H-1

OS-9 LEVEL ONE SYSTEM PROGRAMMERS MANUAL Appendix H: OS-9 Error Codes

OS-9 ERROR CODES (continued)

HEX	DEC

- \$D8 216 MISSING PATHLIST expected pathlist missing or in error.
- \$D9 217 SEGMENT LIST FULL file is too fragmented to be expanded further.
- \$DA 218 FILE ALREADY EXISTS file name already appears in current directory.
- \$DB 219 ILLEGAL BLOCK ADDRESS device's file structure has been damaged.
- \$DC 220 ILLEGAL BIOCK SIZE device's file structure has been damaged.
- \$DD 221 MODULE NOT FOUND request for link to module not found in directory.
- \$DE 222 SECTOR OUT OF RANGE device file structure damaged or incorrectly formatted.
- \$DF 223 SUICIDE ATTEMPT request to return memory where your stack is located.
- \$EØ 224 ILLEGAL PROCESS NUMBER no such process exists.
- \$E1 225 ILLEGAL SIGNAL CODE.
- \$E2 226 NO CHILDREN can't wait because process has no children.
- \$E3 227 ILLEGAL SWI CODE must be 1 to 3.
- \$E4 228 KEYBOARD ABORT process aborted by signal code 2.
- \$E5 229 PROCESS TABLE FULL can't fork now.
- \$E6 230 ILLEGAL PARAMETER AREA high and low bounds passed in fork call are incorrect.
- \$E7 231 BACKTRACK ERROR you'll never see this one.

OS-9 LEVEL ONE SYSTEM PROGRAMMERS MANUAL Appendix H: OS-9 Error Codes

OS-9 ERROR CODES (continued)

HEX DEC

\$E8 234 SIGNAL ERROR - receiving process has previous unprocessed signal pending.

-- DEVICE DRIVER/HARDWARE ERRORS --

- SFØ 240 UNIT ERROR device unit does not exist.
- \$F1 241 SECTOR ERROR sector number is out of range.
- \$F2 242 WRITE PROTECT device is write protected.
- \$F3 243 CRC ERROR CRC error on read or write verify.
- \$F4 244 READ ERROR Data transfer error during disk read operation.
- \$F5 245 WRITE ERROR hardware error during disk write operation.
 - \$F6 246 NOT READY device has "not ready" status.
 - \$F7 247 SEEK ERROR physical seek to non-existant sector.
 - \$F8 248 MEDIA FULL insufficient free space on media.
 - \$F9 249 WRONG TYPE attempt to read incompatible media (i.e. attempt to read double-side disk on single-side drive)